

# Stream Runtime Verification

Martin Leucker

Together with the whole TeSSLa Team

(Lukas Convent, Hannes Kallwies, Martin Sachenbacher, Malte Schmitz,  
Daniel Thoma, Volker Stolz, Cesar Sanchez, and many others)



UNIVERSITÄT ZU LÜBECK  
INSTITUTE FOR SOFTWARE ENGINEERING  
AND PROGRAMMING LANGUAGES

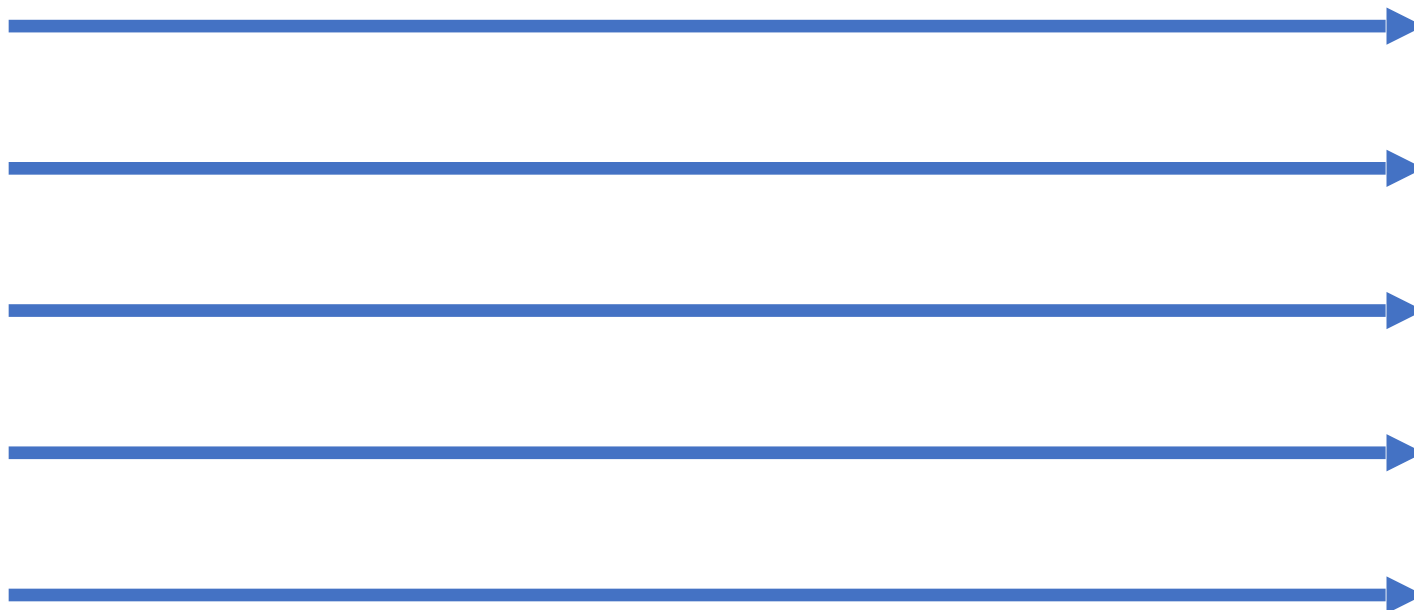


# Plan

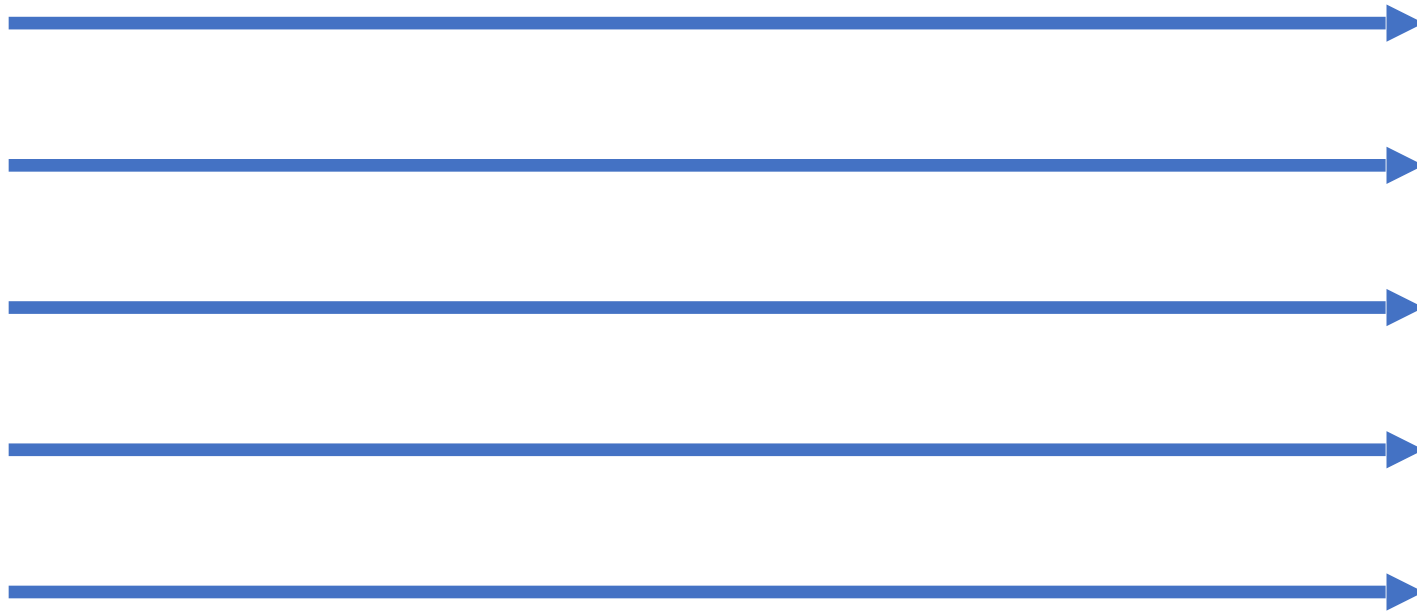
- Stream Runtime Verification
- LOLA
- TeSSLa
  - Language
  - Eco-System
- Control
  - Cyber-Physical Systems
  - Controllers
  - TeSSLa/ROS bridge

Motivation

# Streams

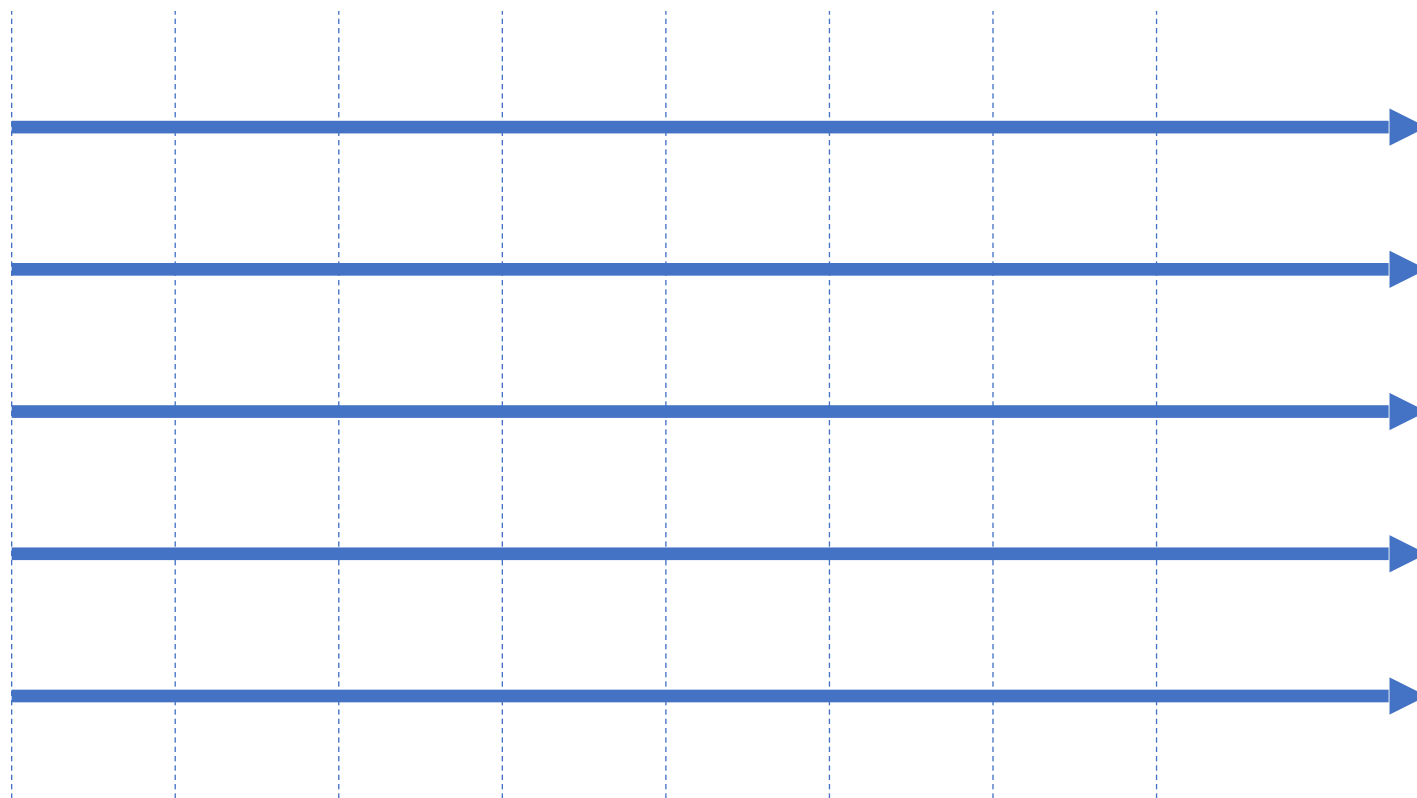


# Streams

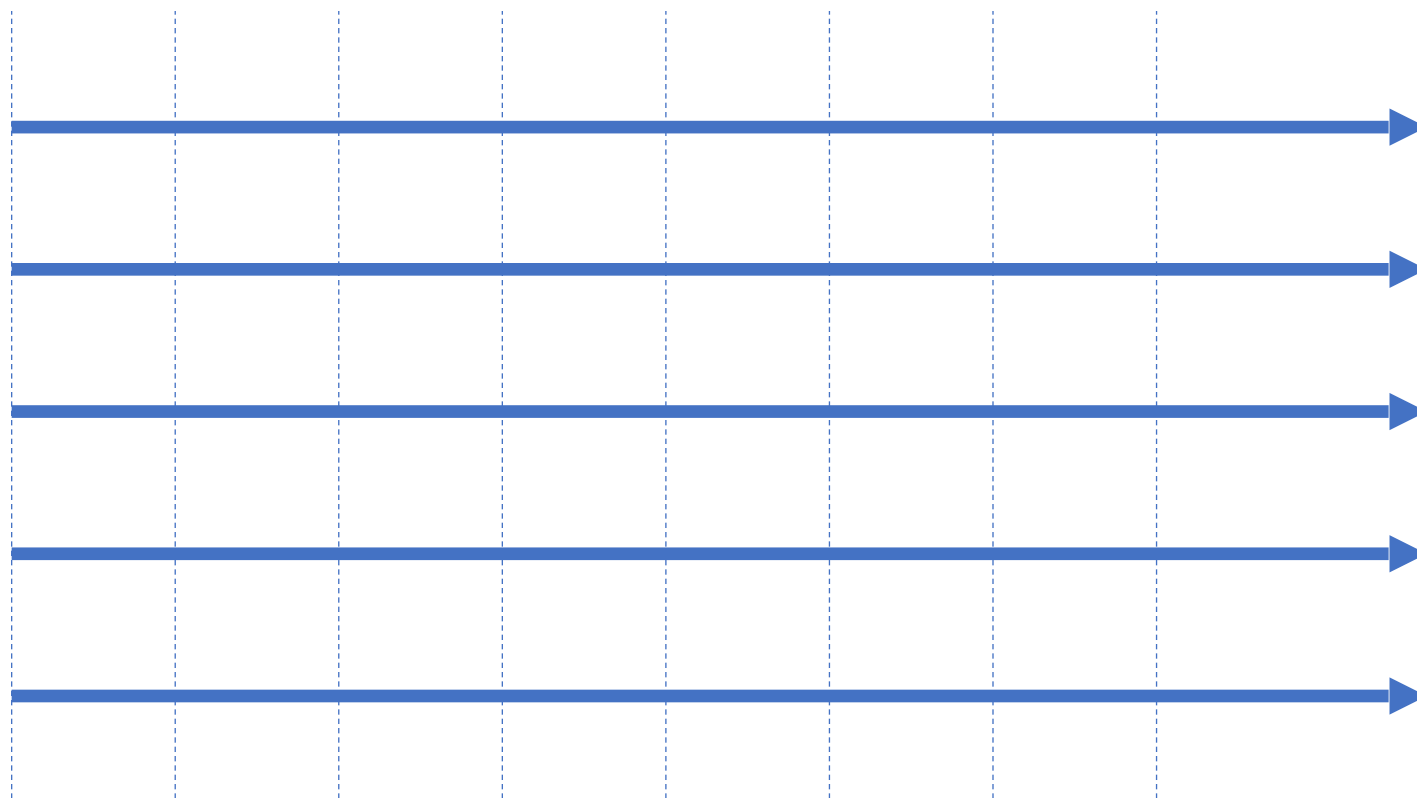


Concurrency/Distribution

# Streams



# Streams



Time? Synchrony/Ticks

# Equational specifications, data, time, concurrency

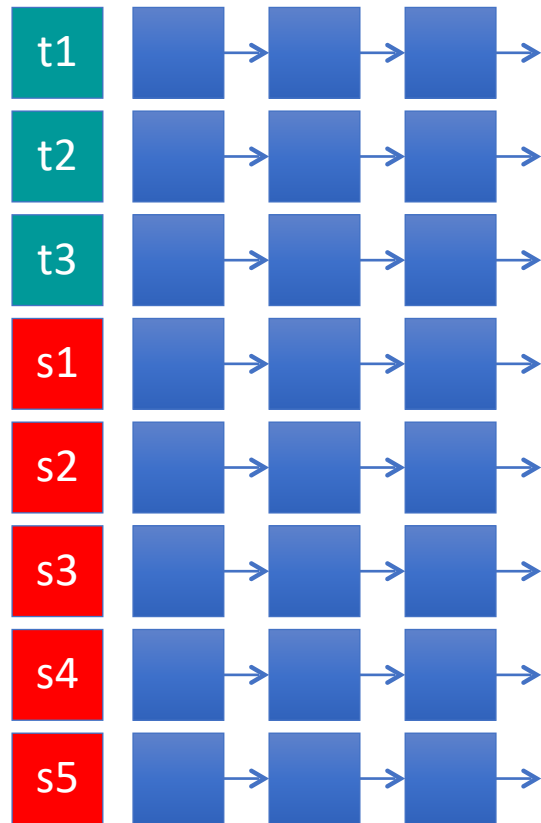
## LOLA

[D'Angelo et al.]

$$\begin{aligned} s_1 &= \mathbf{true} \\ s_2 &= t_3 \\ s_3 &= t_1 \vee (t_3 \leq 1) \\ s_4 &= ((t_3)^2 + 7) \text{ mod } 15 \\ s_5 &= \mathbf{ite}(s_3, s_4, s_4 + 1) \\ s_6 &= \mathbf{ite}(t_1, t_3 \leq s_4, \neg s_3) \\ s_7 &= t_1[+1, \mathbf{false}] \\ s_8 &= t_1[-1, \mathbf{true}] \\ s_9 &= s_9[-1, 0] + (t_3 \text{ mod } 2) \\ s_{10} &= t_2 \vee (t_1 \wedge s_{10}[1, \mathbf{true}]) \end{aligned}$$



# Example



$$s_1 = \mathbf{true}$$

$$s_2 = t_3$$

$$s_3 = t_1 \vee (t_3 \leq 1)$$

$$s_4 = ((t_3)^2 + 7) \text{ mod } 15$$

$$s_5 = \mathbf{ite}(s_3, s_4, s_4 + 1)$$

$$s_6 = \mathbf{ite}(t_1, t_3 \leq s_4, \neg s_3)$$

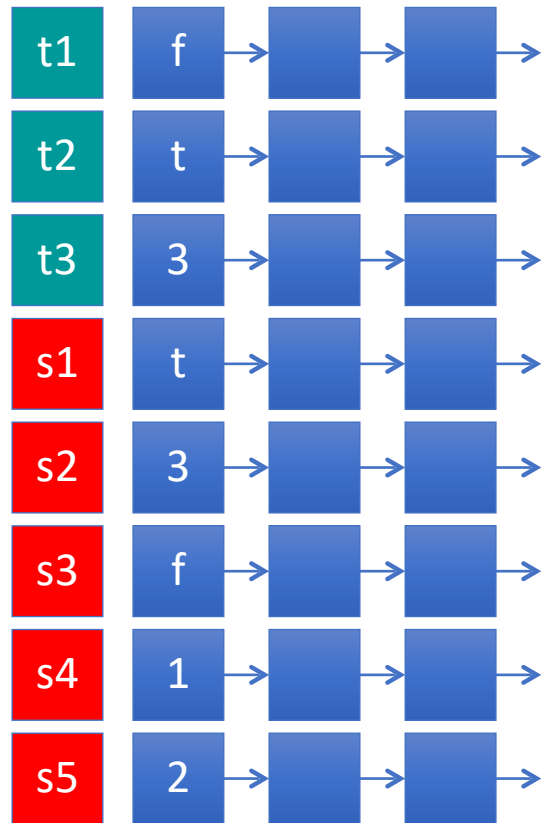
$$s_7 = t_1[+1, \mathbf{false}]$$

$$s_8 = t_1[-1, \mathbf{true}]$$

$$s_9 = s_9[-1, 0] + (t_3 \text{ mod } 2)$$

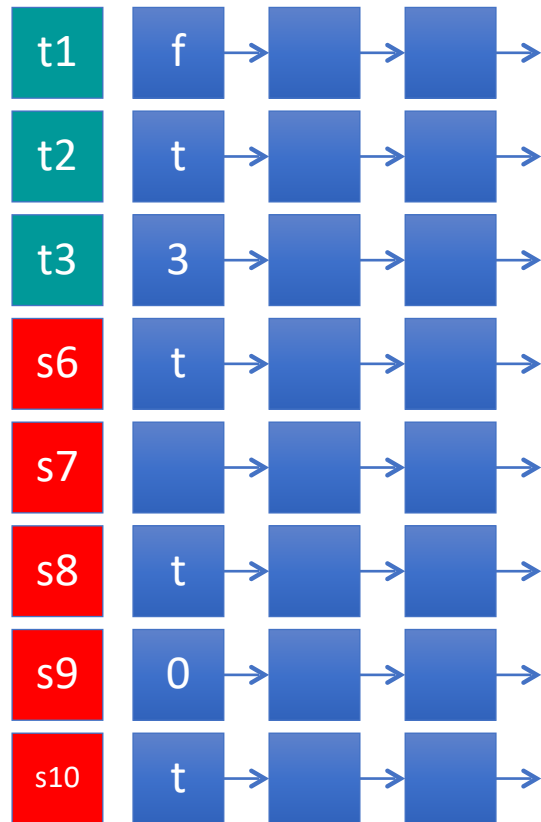
$$s_{10} = t_2 \vee (t_1 \wedge s_{10}[1, \mathbf{true}])$$

# Example



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# Example – Reaching the end of the Trace



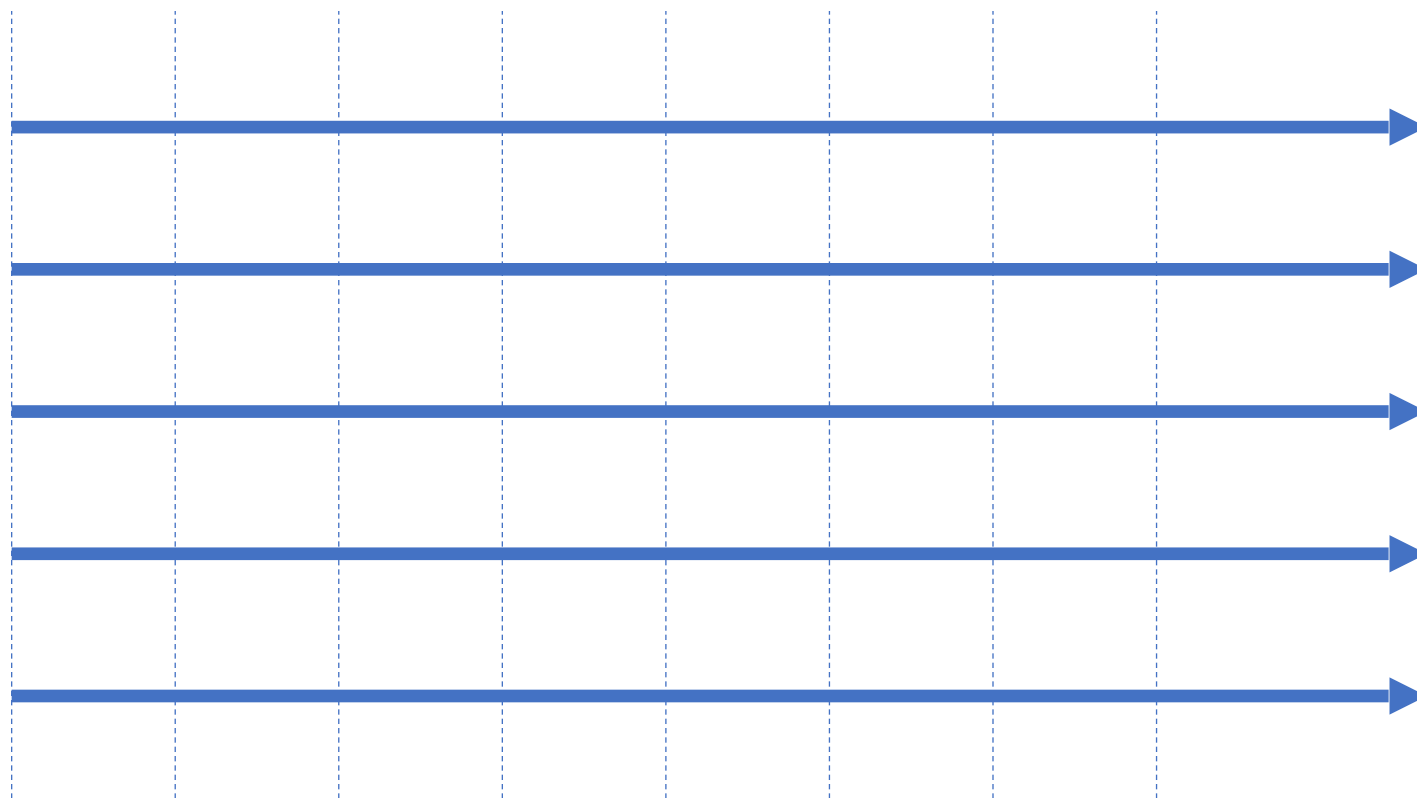
$$\begin{aligned}
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 s_3 &= t_1 \vee (t_3 \leq 1) \\
 s_4 &= ((t_3)^2 + 7) \text{ mod } 15 \\
 s_5 &= \mathbf{ite}(s_3, s_4, s_4 + 1) \\
 s_6 &= \mathbf{ite}(t_1, t_3 \leq s_4, \neg s_3) \\
 s_7 &= t_1[+1, \mathbf{false}] \\
 s_8 &= t_1[-1, \mathbf{true}] \\
 s_9 &= s_9[-1, 0] + (t_3 \text{ mod } 2) \\
 s_{10} &= t_2 \vee (t_1 \wedge s_{10}[1, \mathbf{true}])
 \end{aligned}$$

# Defining new Streams

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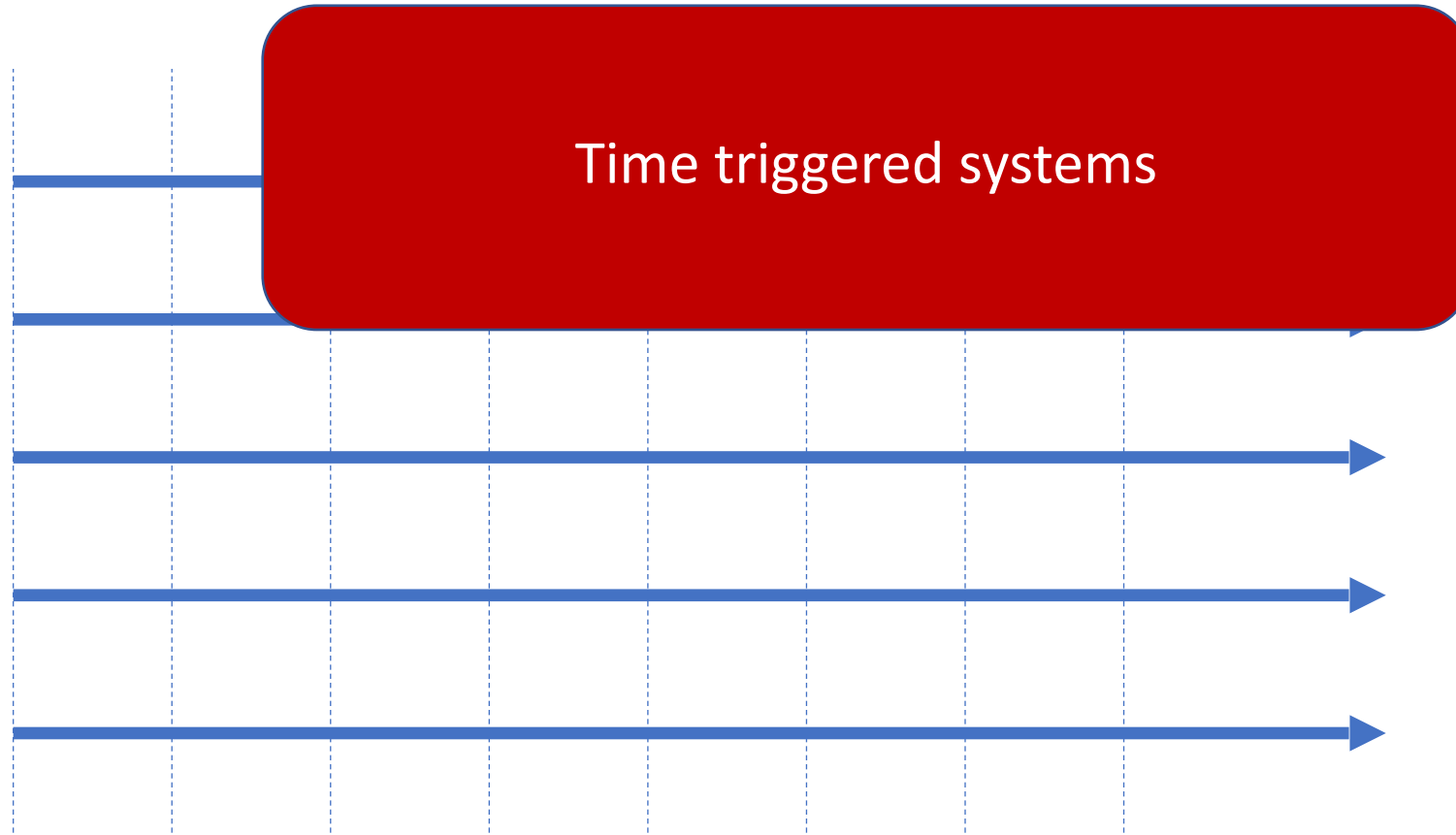
Runtime Verification as Stream Transformation

# Streams



Time? Synchrony/Ticks

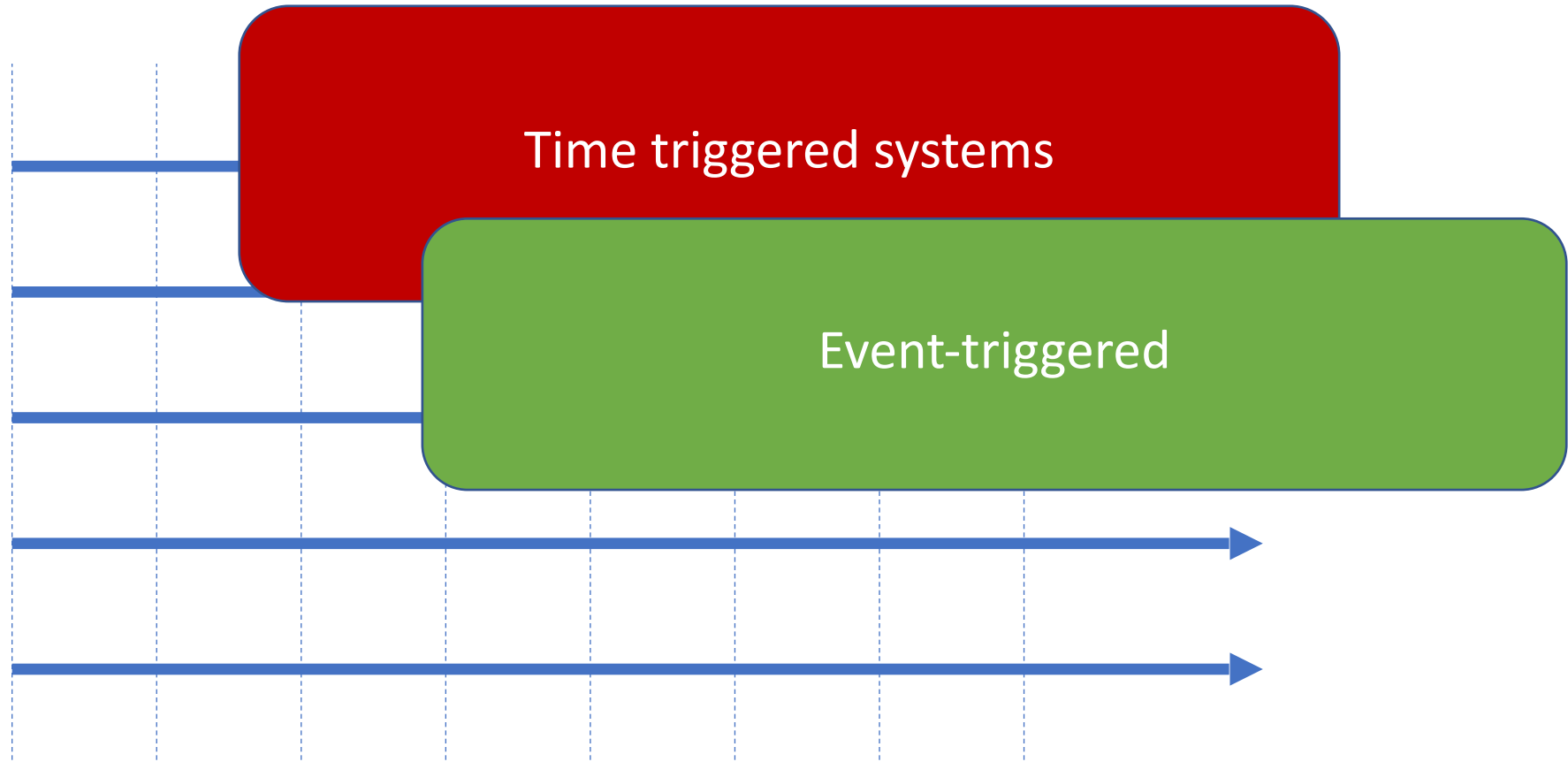
# Streams



Time? Synchrony/Ticks



# Streams



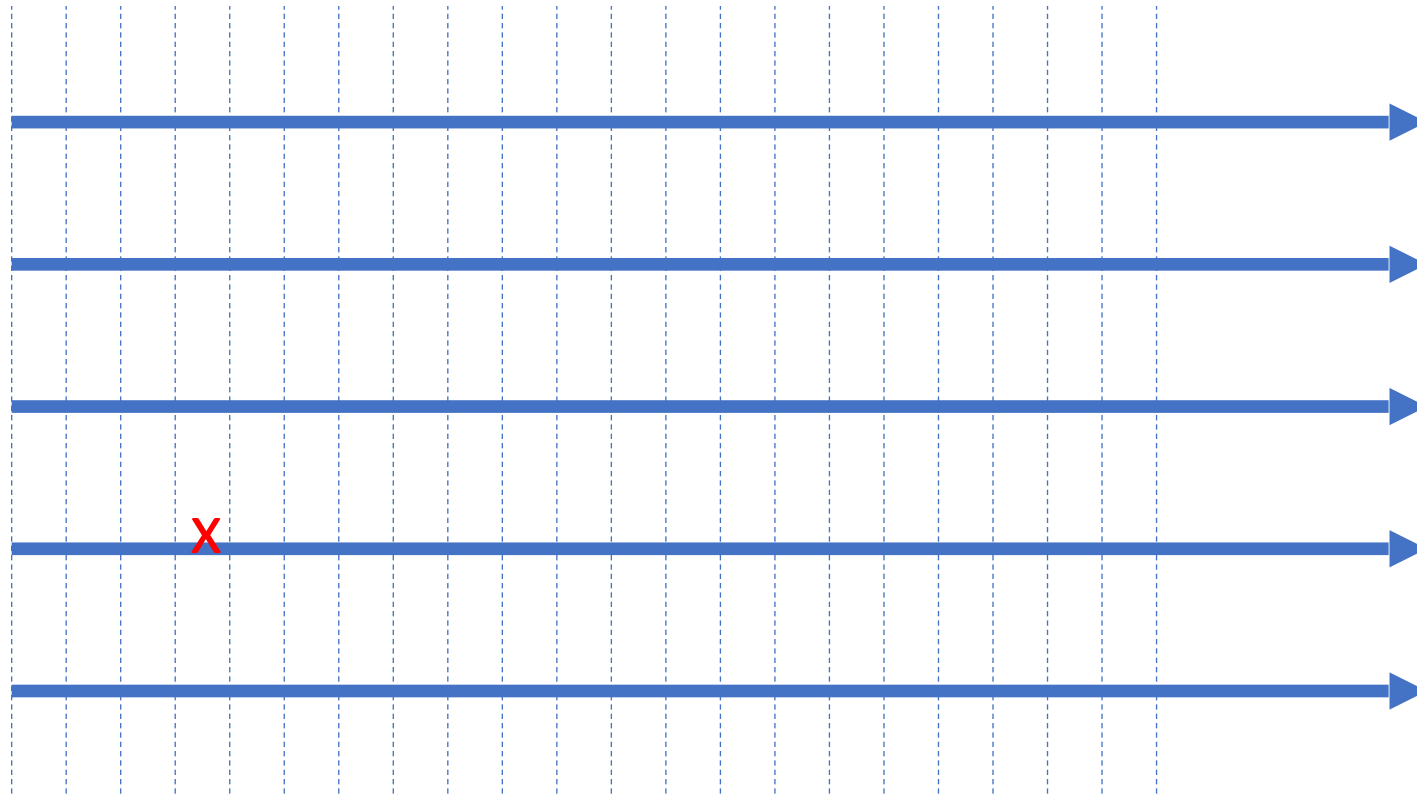
Time? Synchrony/Ticks

# Tesla's Streams



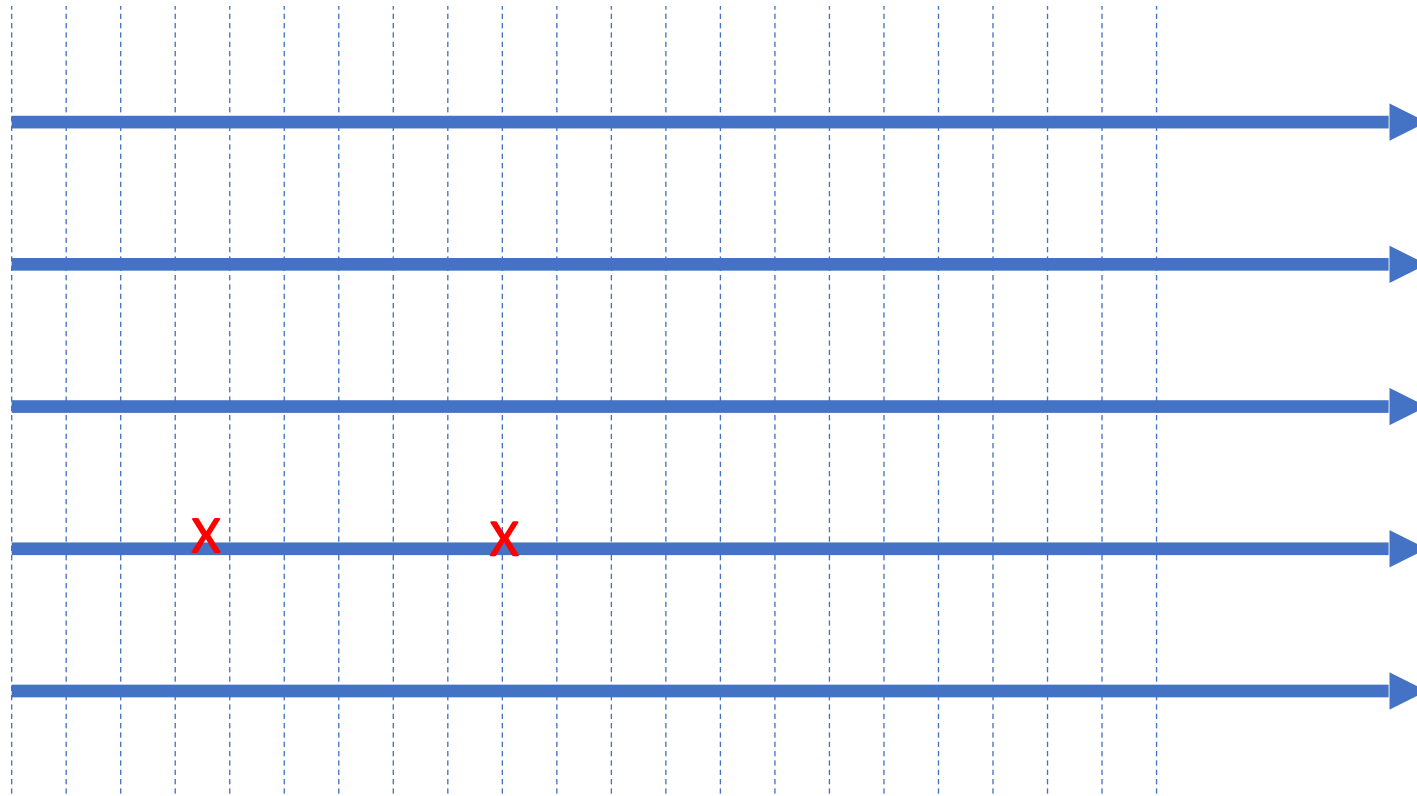
Time? Events

# Tesla's Streams



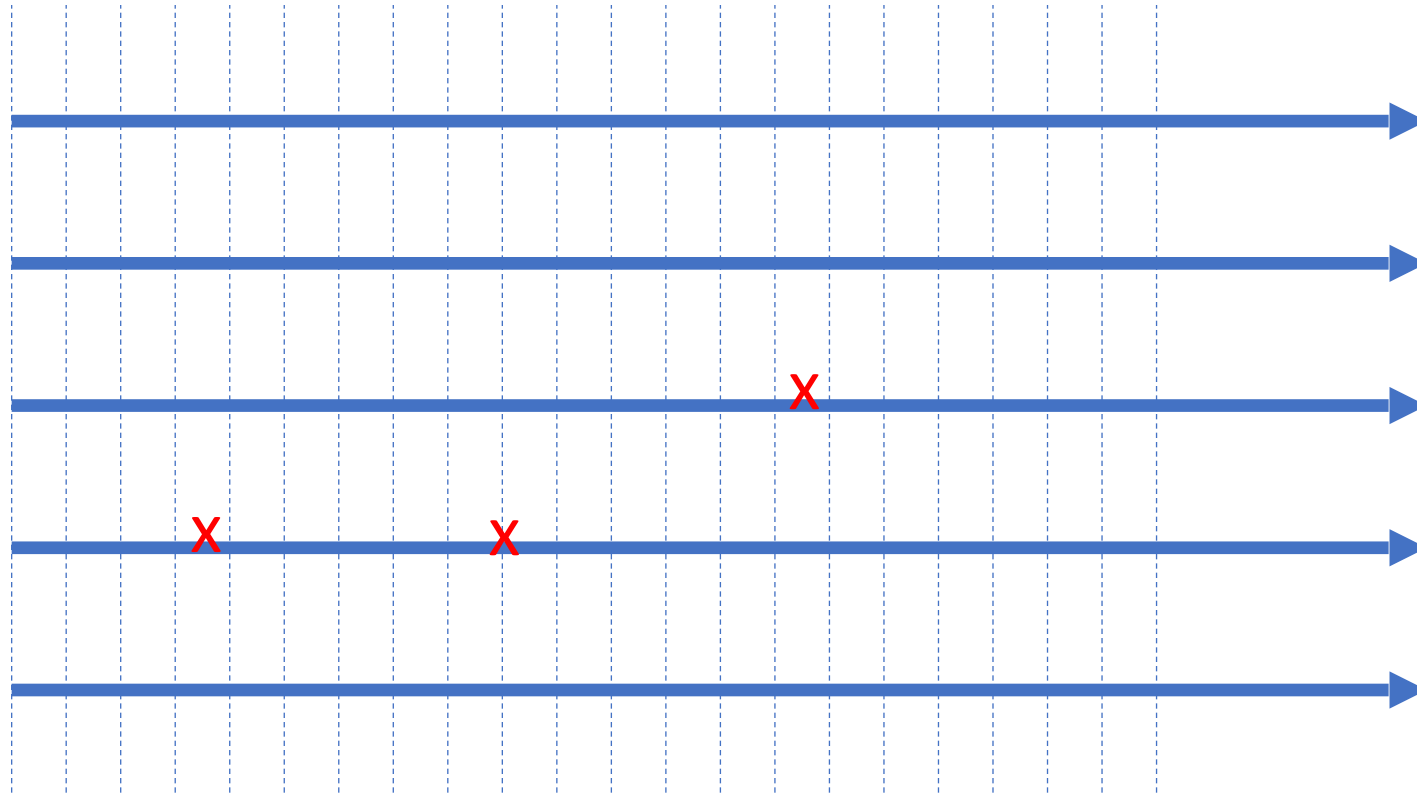
Time? Events

# Tesla's Streams



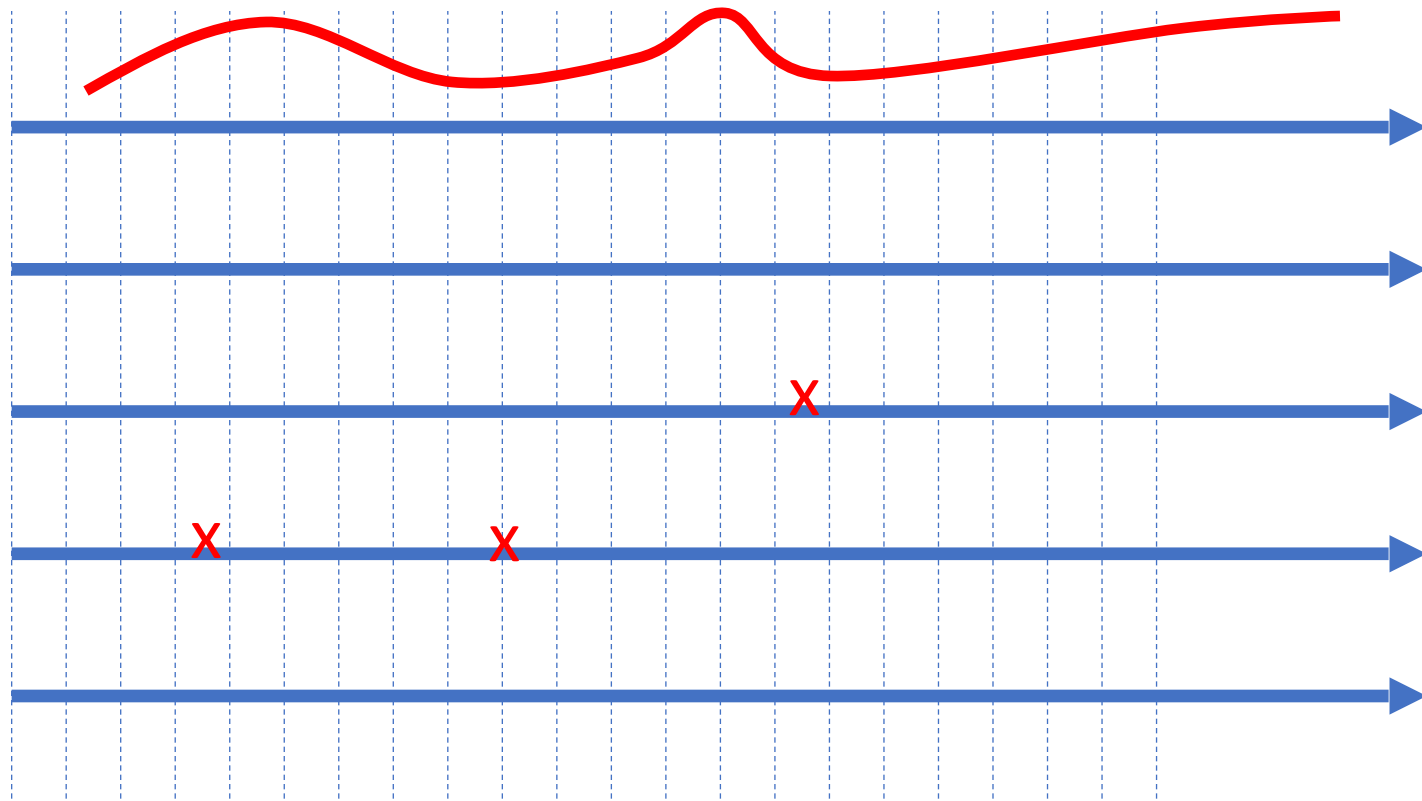
Time? Events

# Tesla's Streams



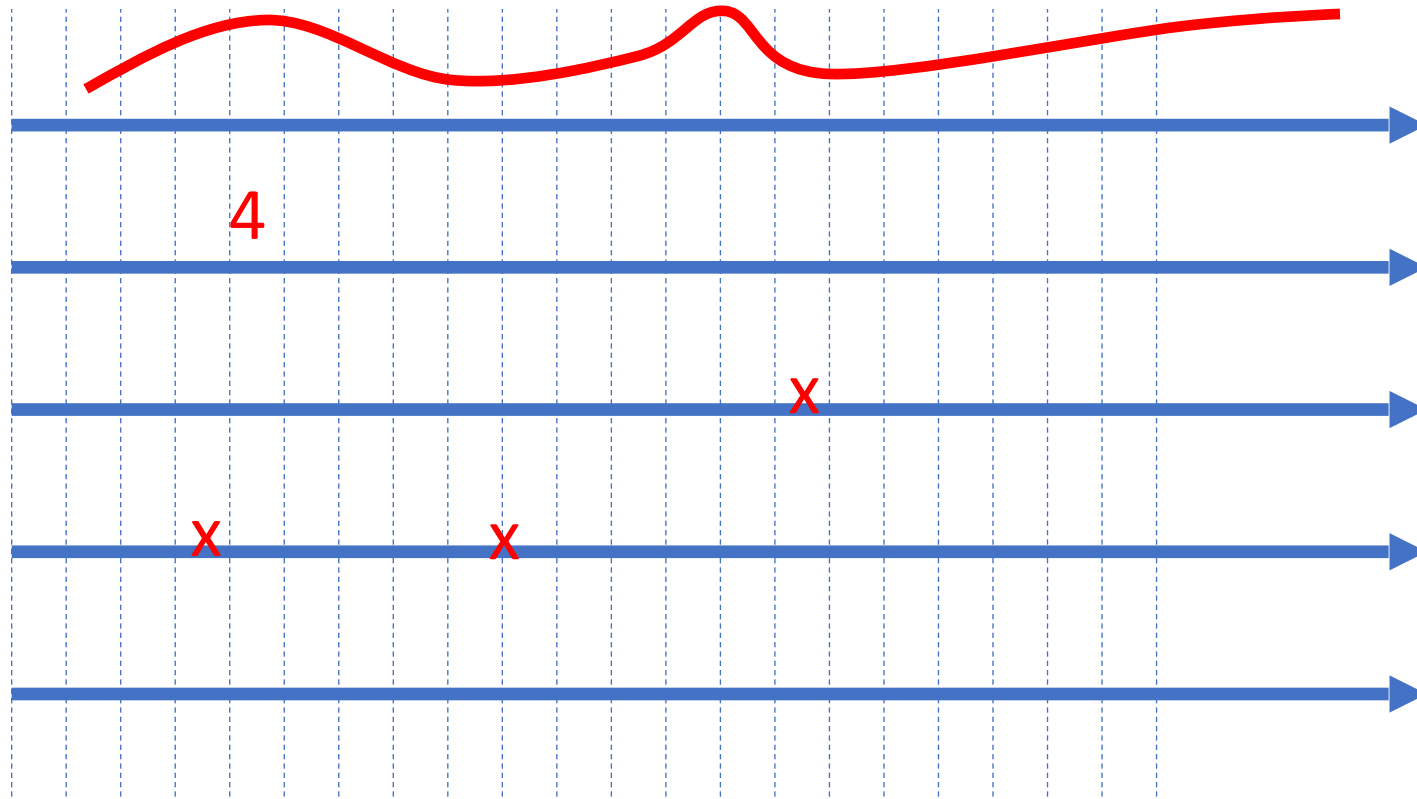
Time? Events

# Tesla's Streams



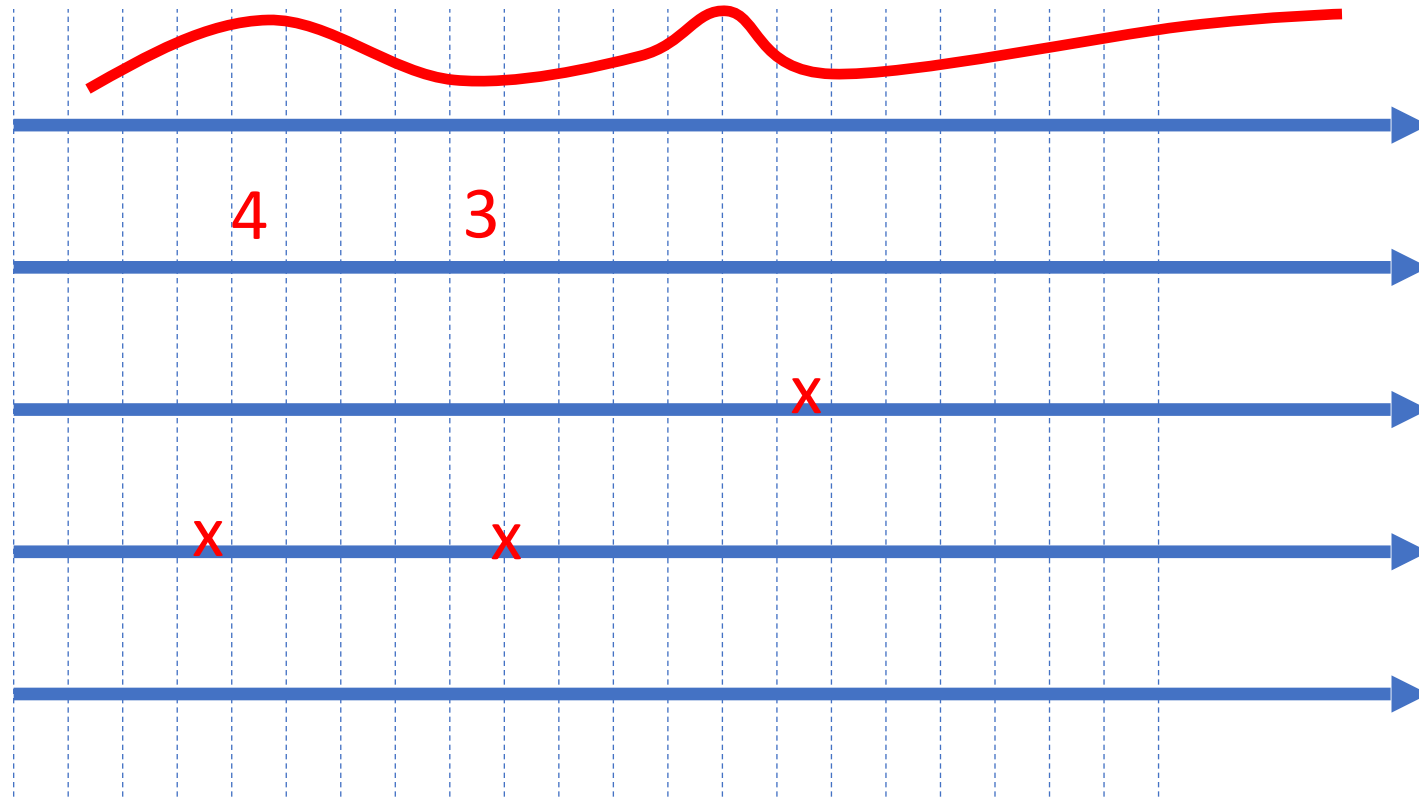
Time? Events

# Tesla's Streams



Time? Events

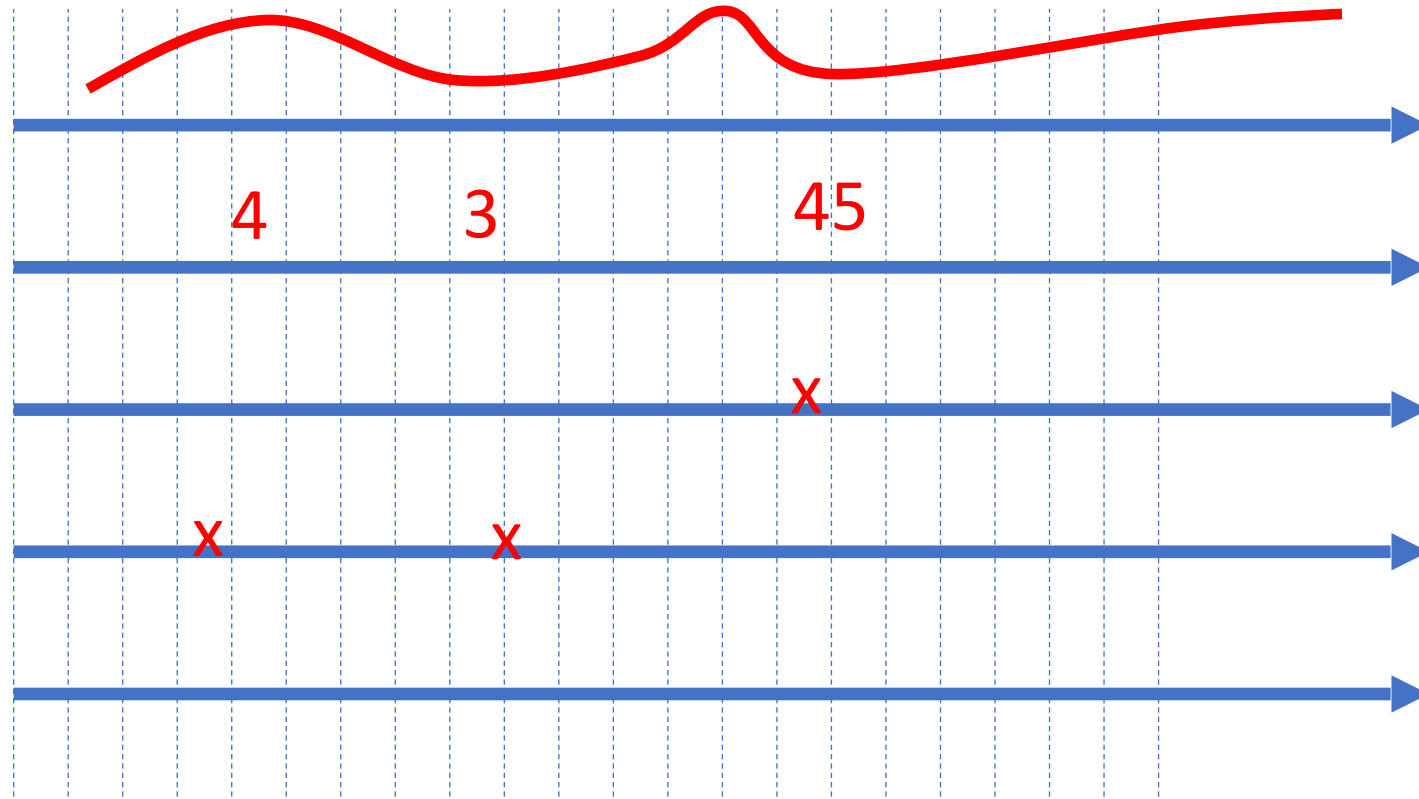
# Tesla's Streams



Time? Events



# Tesla's Streams



Time? Events

# Streams of Programs - After Discretization

Values

e.g., of a program  
variable  $x$



Program events

e.g., call to `my_func()`



# Streams

## Values

e.g., of a program  
variable  $x$

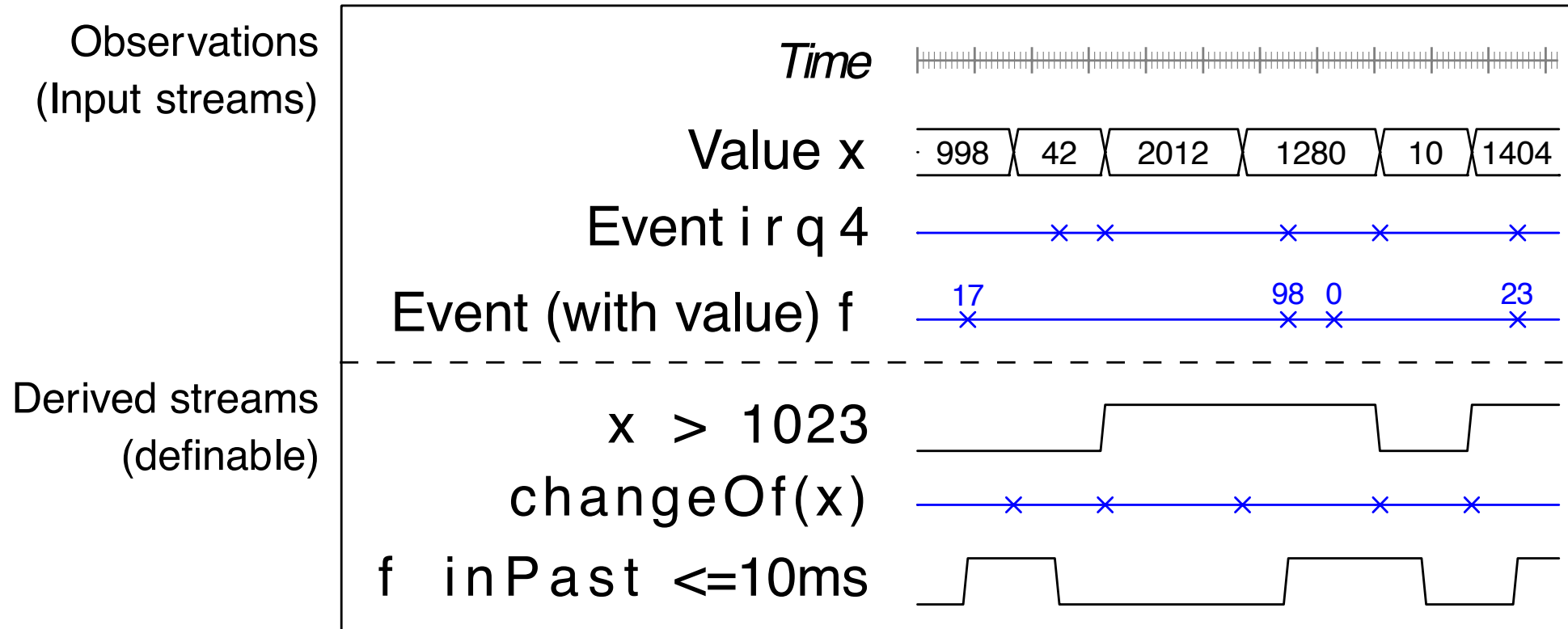


## Program events

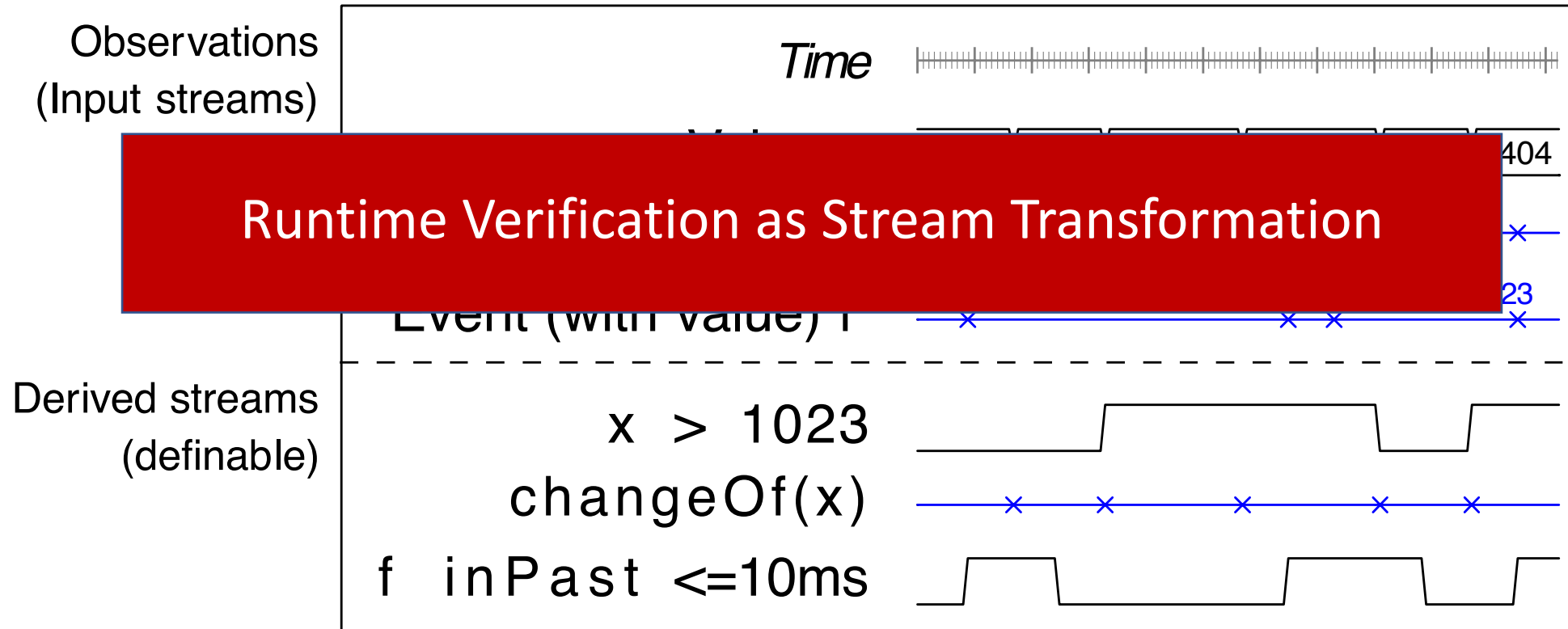
e.g., call to `my_func()`



# Defining new Streams



# Defining new Streams



# Runtime Verification with Uncertainties

# Lola Example

In  $ld$ : Real  $\vdash$   $\text{---} \textcircled{3} \text{---} \textcircled{4} \text{---} \textcircled{5} \text{---} \textcircled{7} \text{---} \rightarrow$

Def  $acc := acc[-1|0] + ld[now] - ld[-3|0]$   $\vdash$   $\text{---} \textcircled{3} \text{---} \textcircled{7} \text{---} \textcircled{12} \text{---} \textcircled{16} \text{---} \rightarrow$

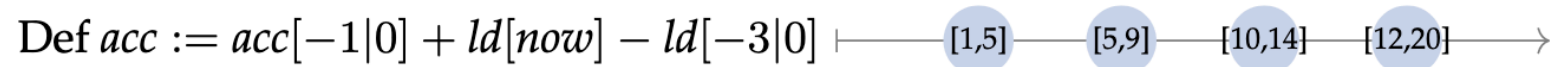
Def  $ok := (acc[now] \leq 15)$   $\vdash$   $\text{---} \textcircled{tt} \text{---} \textcircled{tt} \text{---} \textcircled{tt} \text{---} \textcircled{ff} \text{---} \rightarrow$

Three basic LOLA stream expressions:

- ▶ Constant streams
- ▶ Offset operators  $s[o|c]$   
 $\Rightarrow$ : We restrict our self to the past fragment here (i.e.  $o \leq 0$ )
- ▶ Function applications

# Using Abstract Domains

In 2019 Leucker et al. presented approach with intervals as abstract domain.



- ▶ Approach is sound, but not perfect.
- ▶ Handling of complex assumptions in general not possible.

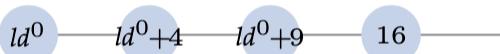


# Symbolic Evaluation

**Idea:** Use symbolic formulas for representation of unknown values and additional logical constraints (e.g. assumptions).

⇒ Use SMT solver for queries on possible values.

In  $ld$ : Real  $\vdash$  

Def  $acc := acc[-1|0] + ld[now] - ld[-3|0]$   $\vdash$  

Def  $ok := (acc[now] \leq 15)$   $\vdash$  

Additional constraints:  $\{1 \leq ld^0 \leq 5\}$

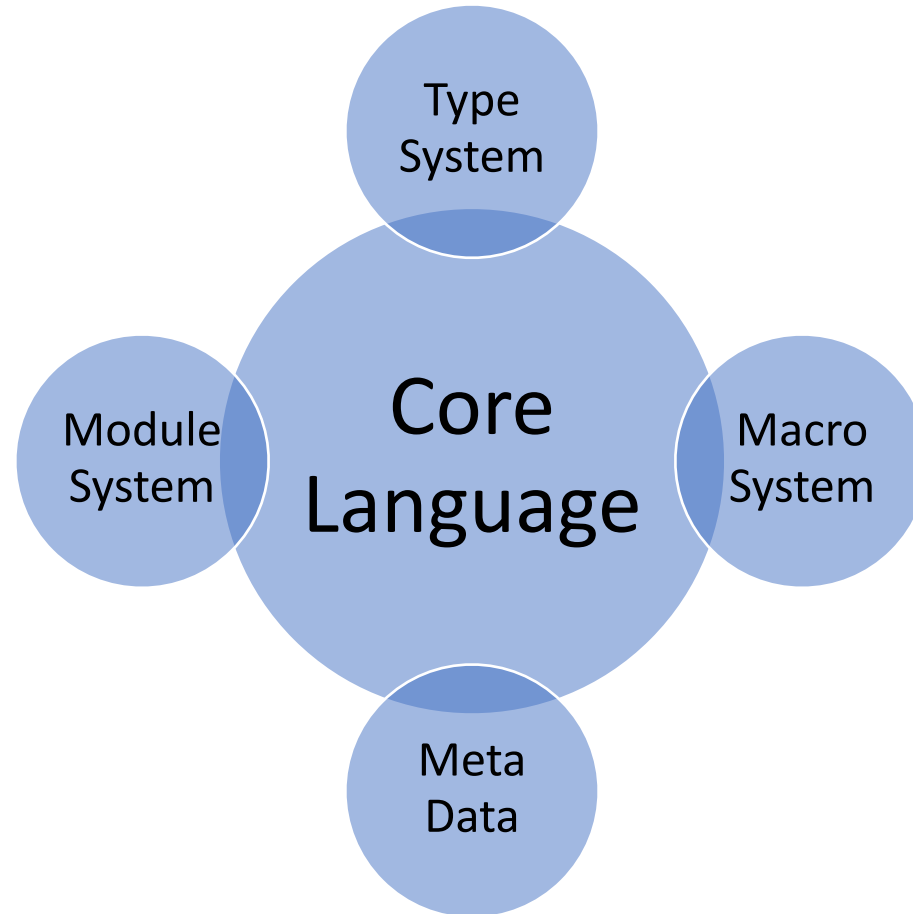
- ▶ Approach in principle perfect.
- ▶ Assumptions can be added as propositions to constraint set.

TeSSLa

# TeSSLa

- **T**emporal
  - **S**tream-based
  - **S**pecification
  - **L**anguage
- 
- Specifying the (expected) behavior of a system's execution

# Language - Overview



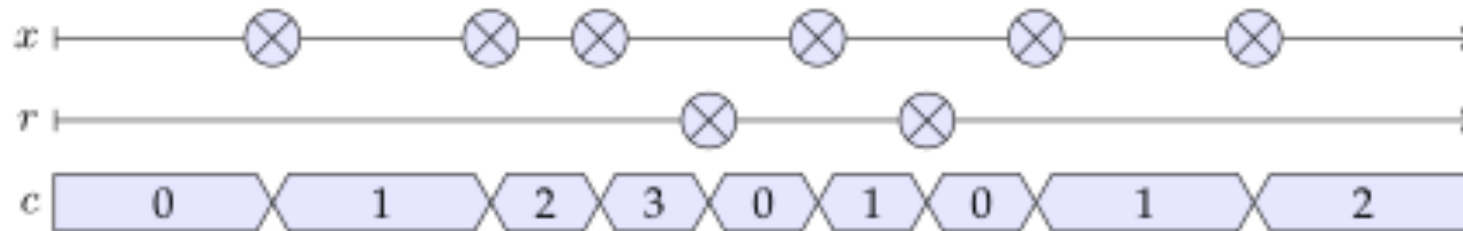
# Design Goals – Core Language

- Declarative style: Specification rather than implementation
- Modularity: Allowing abstractions based on few primitives  
(6 operators: `unit`, `nil`, `lift`, `last`, `delay`, `time`)
- Time as first-class citizen
- Abstractions for both events and signals
- Recursion to reason about past
- Implementable with limited memory  
(For a restricted fragment)

# TeSSLa by example



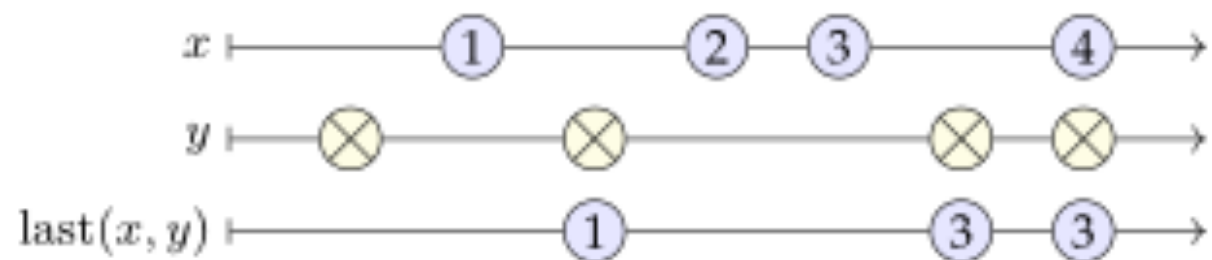
```
def c := a + b
```



```
def c := eventCount(x, reset = r)
```

# TeSSLa operators: Last

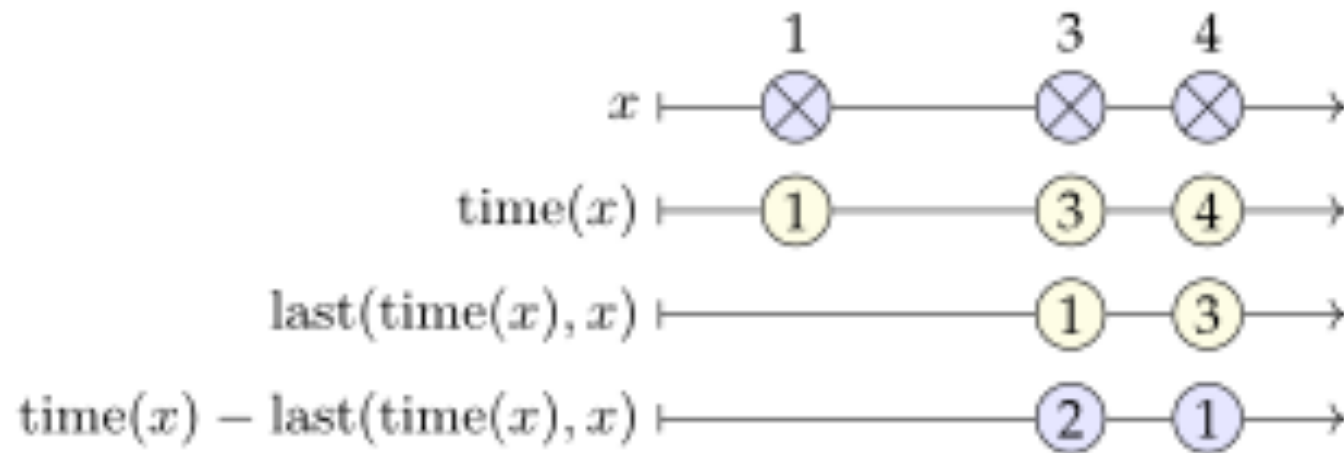
- ▶ Needed to define properties over sequences of events.
- ▶ Last allows to refer to the values of events on one stream that occurred strictly before the events on another stream



Read  $\text{last}(x, y)$  as last of  $x$  when event on  $y$

# TeSSLa operators: Time

- ▶ Provides access to the *timestamps* of events
- ▶ Produces events carrying their *timestamps as data value*
- ▶ Hence *all operators* for data values can be applied to timestamps.





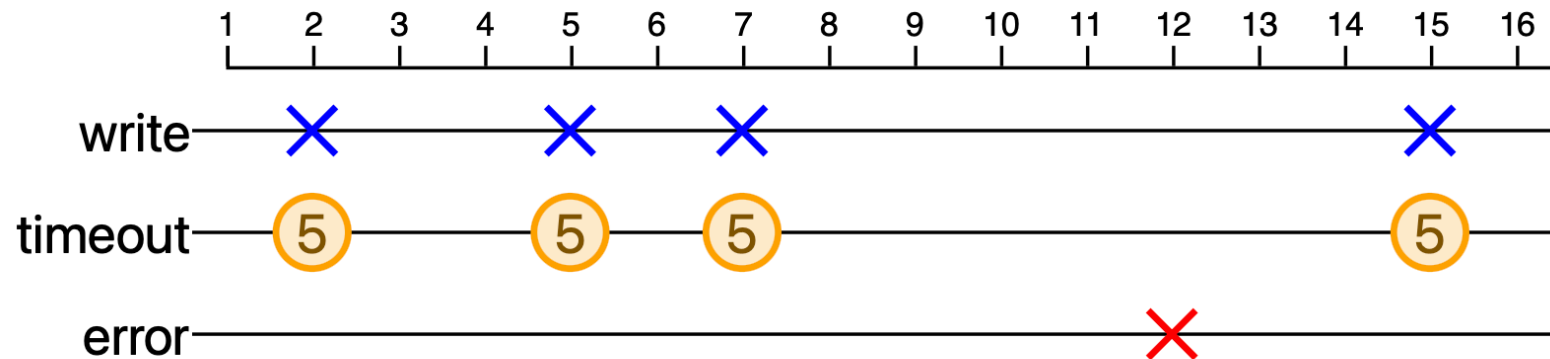
# Create Events

```
in write: Events[Unit]
```

```
def timeout := const(5, write)
```

```
def error := delay(timeout, write)
```

```
out error
```



# Data types in TeSSLa

- TeSSLa strongly typed, generic types
- TeSSLa **agnostically** wrt **any time or data domain**
- **Different data structures** can be used to **represent time and data**
- Monitoring in hardware:  
**atomic data types**, e.g. int or float
- Monitoring in software:  
**complex data structures** like lists, trees and maps

# Macros in TeSSLa

- Few primitive operators
- Readable specifications via Macros
- TeSSLa Standard Library for common useful stuff
- Domain specific libraries for application areas/domains (anticipated)
  - Timex/Autosar library
  - PastLTL
  - Petri nets (under development)

# Macros in TeSSLa: EventCount

```
# Count the number of events on `values`.
def eventCount[A,B](values: Events[A]) := {
  def count: Events[Int] := merge(
    # increment counter
    last(count, values) + 1
  , 0)
  count
}
```

# Modules in TeSSLa

- Sets of Macros can be grouped to modules/libraries
- TeSSLa Standard Library for common useful stuff
- Domain specific libraries for application areas/domains (anticipated)
  - Timex/Autosar library
  - PastLTL
  - Petri nets (under development)

# Meta Data / Annotations

- TeSSLa allows annotations similar like @interface in Java
- Several categories for annotations
  - Documentation
  - Correspondence to Source Code (C-Code)
  - Graphical presentation of streams / dashboard support
  - Directives for Example Generator
  - Directives for bridging to frameworks (ROS)

# TeSSLa by Example

```
# Inputs
@InstFunctionCall("read_brake_sensor")
in read_brake_sensor: Events[Unit]
@InstFunctionCall("activate_brakes")
in activate_brakes: Events[Unit]
```

} Input decl.  
&  
annotations

```
# Trace Processing
def latency = measureLatency(read_brake_sensor,
                             activate_brakes)

def error = latency > 4ms
def high = filter(latency, error) - 4ms
def is_critical = count(high) > 10
def critical = filter(high, is_critical)
```

} Monitoring  
property

```
# Output
@VisDots out high
@VisEvents out critical
```

} Output decl.  
&  
annotations

```
# Macro
def measureLatency[A, B](a: Events[A],
                         b: Events[B]) =
    time(b) - last(time(a), b)
```

} Macro  
definitions

# TeSSLa by Example

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# Inputs
@InstFunctionCall("read_brake_sensor")
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annotations

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Macro  
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} Monitoring  
property

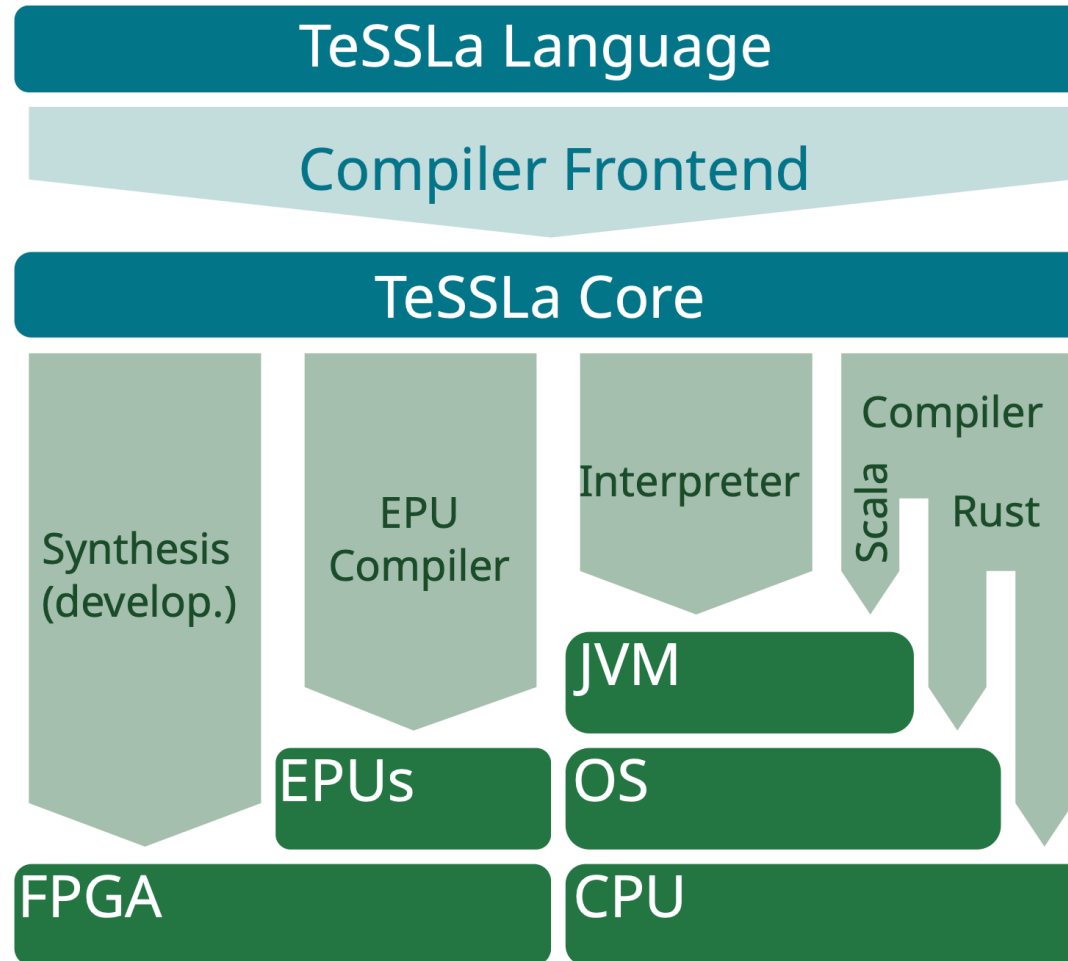
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} Output decl.  
&  
annotations

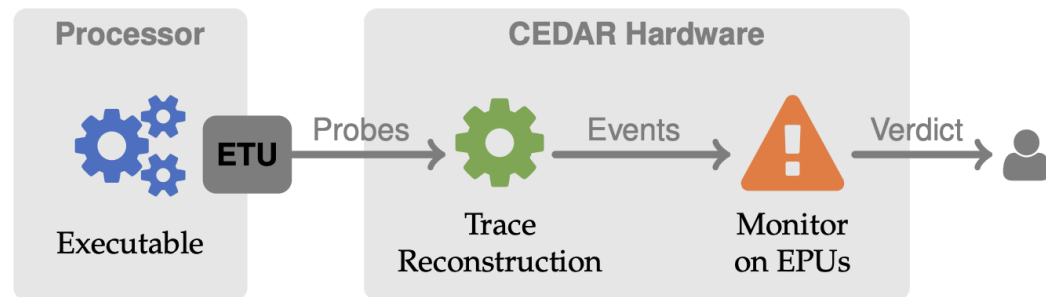
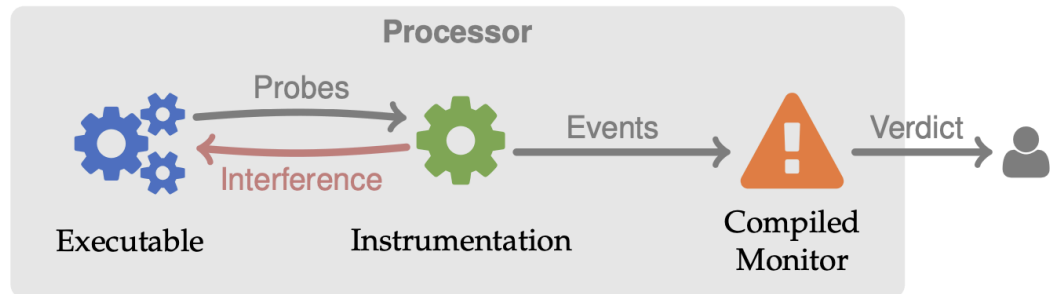
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def measureLatency[A, B](a: Events[A],
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```

} Macro  
definitions

# TeSSLa compilers



# Observation/Instrumentation



- Instrumenter for C code integrated in compiler
- Accemic's CEDARtools for non-intrusive hardware monitoring
- Connection to other instrumentation tools via generic annotation system

# Supporting Web IDE

The screenshot displays the TeSSLa web IDE interface in a browser window. The browser address bar shows the URL `play.tessla.io`. The interface includes a navigation bar with the TeSSLa logo, a 'Run' button, and links for 'About', 'TeSSLa Examples', 'RV Examples', and 'Settings'.

The main workspace is divided into several panels:

- Trace / C Code:** Shows C code for a task. The visible code includes:

```
26 void run_task()
27 {
28     /* Sample Brake Sensor */
29     float brake_angle = read_brake_sensor();
30
31     /* Process */
32     int strength;
33     strength = calculate_brake_strength_for_angle(brake_angle);
34
35     int motor_control;
36     motor_control = calculate_motor_control_value(strength);
```
- Specification:** Shows the formal specification for the task:

```
1 # Inputs
2 @InstFunctionCall("read_brake_sensor")
3 in read_brake_sensor: Events[Unit]
4 @InstFunctionCall("activate_brakes")
5 in activate_brakes: Events[Unit]
6
7 # Trace Processing
8 def latency = measureLatency(read_brake_sensor, activate_brakes)
9 def error = latency > 4ms
10 def high = filter(latency, error) - 4ms
11 def is_critical = count(high) > 10
```
- Status and Compiler Output:** Shows the execution status:

```
1 STATUS verifying spec.tessla and instrumenting C code...
2 STATUS compiling C code...
3 STATUS starting /tmp/bin/main...
4
```
- TeSSLa Output / TeSSLa Visualization:** A visualization of the execution trace. The x-axis represents time in units of 10,000,000,000. The y-axis shows 'high' (blue dots) and 'critical' (blue 'x' marks). A specific data point is highlighted with a yellow box: `30771608745: 261155`.

# Supporting Online Documentation

## constIf

```
constIf[T](value: T, condition: Events[Bool]): Events[T]
```

Produce an event with the given value every time that the condition is met

Usage example:

```
in condition: Events[Bool]
def result = constIf(42, condition)
out result
```

Trace example:



Source

```
def constIf[T](value: T, condition: Events[Bool]): Events[T] =
  filter(const(value, condition), condition)
```

## count

```
count[T](x: Events[T]): Events[Int]
```

Count the number of events on `x`. Provides for every input event an output event whose value is the number of events seen so far. See [resetcount](#) for a counting macro with an external reset.

# TeSSLa Ecosystem

- User Libraries
  - Macro system allows definition of application-specific libraries
  - E.g. AUTOSAR Timex, Past LTL libraries...
- Tutorials
  - Extensive tutorials about the usage of the TeSSLa language and tools.
- Open-Source availability
  - Free availability of most parts of the tool chain.
  - Community-driven project.

# TeSSLa for professional usage

- Clear definition of license
- Separation of
  - Language,
  - Compilers, and
  - Tools
- Language specification
  - TeSSLa and TeSSLa Core
- Reference Compiler (Interpreter)



# Resources

- TeSSLa Website:

<https://www.tessla.io/>

- TeSSLa Playground:

<https://play.tessla.io/>

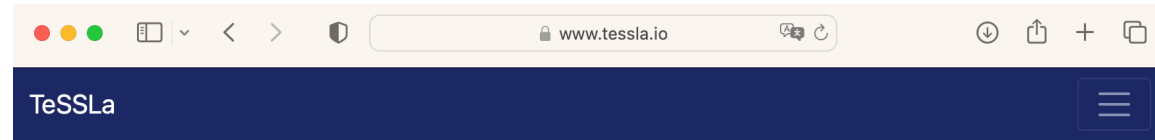
- TeSSLa Sourcecode:

<https://git.tessla.io/>

- Contact:

[info@tessla.io](mailto:info@tessla.io)

tessla.io



A Convenient Language for  
Specification and Verification  
of Your System

# TeSSLa Installation and First-Steps

# Installation – TeSSLa Bundle

- contains a compiler, interpreter and other useful tools for executing TeSSLa specifications
- written in [Scala](#) and available as a single JAR archive.
- The TeSSLa bundle is licensed under [Apache 2.0 license](#).  
Run `java -jar tessla.jar -h` for information on the usage of the TeSSLa command line tool.

<https://git.tessla.io/tessla/tessla/builds/artifacts/master/raw/target/scala-3.2.2/tessla-assembly-2.0.0.jar?job=deploy>

# Logging Library

- For instrumenting C-Code

<https://www.tesla.io/logging.zip>

# TeSSLa libraries

Futher libraries

<https://www.tessla.io/usrLibs/overview/>



## TeSSLa TADL2/Autosar-Timex Library

**TeSSLa version:** 1.2.2-1.2.4, **License:** [Apache 2.0](#)

*TeSSLa library with functions for checking TADL 2 Constraints*

[Download](#), [Documentation](#) [Project Page](#)



## TeSSLa TDDL Library

**TeSSLa version:** 1.2.2-1.2.4, **License:** [Apache 2.0](#)

*TeSSLa implementation of Timed Dyadic Deontic Logic*

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## TeSSLa/ROS Bridge

**TeSSLa version:** 1.2.2+, **License:** [Apache 2.0](#)

*TeSSLa library and tooling for integration with the Robot Operating System (ROS)*

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## TeSSLa Telegraf Connector

**TeSSLa version:** 1.2.3+, **License:** [Apache 2.0](#)

*TeSSLa library and tooling for integration with the Telegraf framework*

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# A simple specification

- specification.tesla

```
in x: Events[Int]
```

```
in y: Events[Int]
```

```
def diff = sum(x) - sum(y)
```

```
liftable
```

```
def abs(x: Int) = if x < 0 then -x else x
```

```
def tooBig = abs(diff) >= 10
```

```
out diff
```

```
out tooBig
```

# Input trace

- trace.input

10:  $x = 2$

17:  $x = 1$

19:  $y = 4$

37:  $x = 7$

45:  $x = 6$

78:  $y = 9$

98:  $x = 2$



# In the playground

<https://play.tesla.io>

# Playground

The screenshot displays the TeSSLa Playground interface in a browser window. The browser's address bar shows the URL `play.tessla.io`. The page header includes the TeSSLa logo, a `Run` button, and navigation links for `About`, `TeSSLa Examples`, `RV Examples`, and `Settings`.

The interface is divided into four main panels:

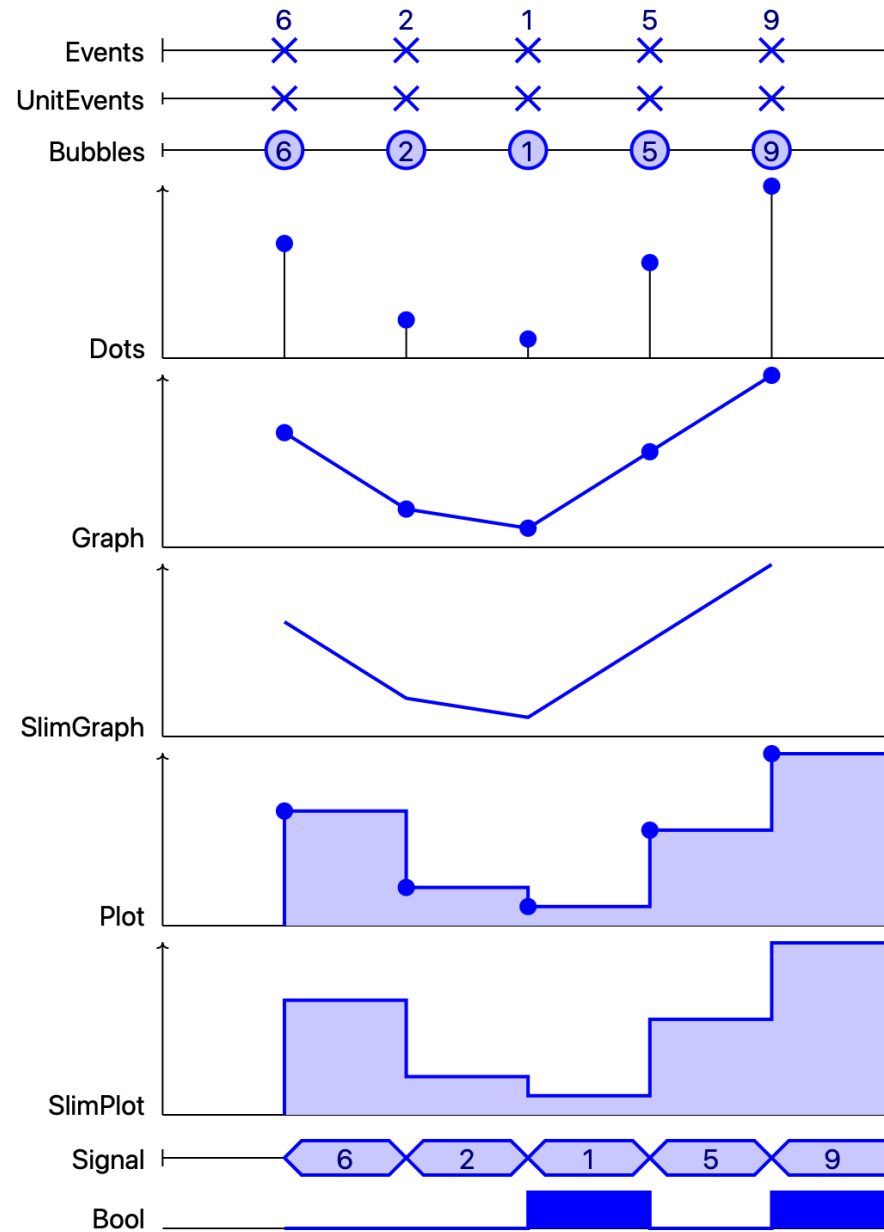
- Trace:** Shows a sequence of events with timestamps and variable assignments:

```
1 10: x = 2
2 17: x = 1
3 19: y = 4
4 37: x = 7
5 45: x = 6
6 78: y = 9
7 98: x = 2
8
```
- Specification:** Contains the formal specification code:

```
1 in x: Events[Int]
2 in y: Events[Int]
3
4 def diff = sum(x) - sum(y)
5
6 liftable
7 def abs(x: Int) = if x < 0 then -x else x
8 def tooBig = abs(diff) >= 10
9
10 @VisSignal
11 out diff
12 out tooBig
13
```
- Status and Compiler Output:** Currently empty, showing only the number `1`.
- TeSSLa Output:** Displays the results of the simulation, including the `tooBig` and `diff` values at each time step:

```
3 10: tooBig = false
4 10: diff = 2
5 17: tooBig = false
6 17: diff = 3
7 19: tooBig = false
8 19: diff = -1
9 37: tooBig = false
10 37: diff = 6
11 45: tooBig = true
12 45: diff = 12
13 78: tooBig = false
14 78: diff = 3
15 98: tooBig = false
16 98: diff = 5
17
```

@VisSTYLE



# Running

- `java -jar tessla.jar interpreter specification.tessla trace.input`

0: tooBig = false

0: diff = 0

10: tooBig = false

10: diff = 2

17: tooBig = false

17: diff = 3

19: tooBig = false

19: diff = -1

37: tooBig = false

37: diff = 6

45: tooBig = true

45: diff = 12

78: tooBig = false

78: diff = 3

98: tooBig = false

98: diff = 5

# TeSSLa Scala/Rust Compiler

- **Scala compiler**

- allows compilation to Scala code or a JAR file executable on the Java JVM.

```
java -jar tessla.jar compile-scala -j monitor.jar specification.tessla
```

- creates an executable Jar-File **monitor.jar** which receives inputs and produces outputs via stdio in the same format as the interpreter

- **Rust compiler**

```
java -jar tessla.jar compile-rust -b monitor specification.tessla
```

- creates an executable **monitor** which receives inputs and produces outputs via stdio in the same format as the interpreter

# Instrumenting C-Code

- Instrument the C source code using the observation annotations defined in the TeSSLa specification:

```
java -jar tessla.jar instrumenter spec.tessla main.c  
/usr/lib/gcc/x86_64-linux-gnu/9/include/
```

- Instrumentation is done on the LLVM level and specific setup for your machine is needed

# For convenience

- As long as it works

```
docker run -v $(pwd):/wd -w /wd --rm registry.isp.uni-luebeck.de/  
tesla/tesla-docker:2.0.0 rv spec.tesla main.c
```

# TeXLa Language in Detail



# Let's work through the tutorial

<https://www.tessla.io/tutorial/>

RV with TeSSLa

# main.c

```
void foo() {  
    int x = 42;  
}  
  
int main() {  
    for (int i = 0; i < 5; i++) {  
        foo();  
    }  
    return 0;  
}
```

# spec.tesla

```
@InstFunctionCall("foo")  
in foo: Events[Unit]  
out foo  
def num := count(foo)  
out num
```

# Explore

- Instrument the C source

```
java -jar tessler.jar instrumenter spec.tessler main.c  
usr/lib/gcc/x86_64-linux-gnu/9/include/
```

- Compile the instrumented C code

```
gcc main.c.instrumented.c -llogging -pthread -ldl -o main
```

- Execute the compiled program, creating the file trace.log

```
./main
```

- Monitor the trace

```
java -jar tessler.jar interpreter --base-time 1ns spec.tessler trace.log
```

- Alternatively

```
docker run -v $(pwd):/wd -w /wd --rm registry.isp.uni-luebeck.de/tessler/tessler-  
docker:2.0.0 rv spec.tessler main.c
```

# Measuring a Function's Runtime

```
#include <stdlib.h>
#include <unistd.h>

void compute() {
    int duration = 40000;
    duration += (rand() % 10) * 1000;
    usleep(duration);
}

int main() {
    for (int i = 0; i < 10; i++) {
        compute();
    }
}
```

```
@InstFunctionCall("compute")
in call: Events[Unit]

@InstFunctionReturn("compute")
in ret: Events[Unit]

def duration := runtime(call, ret)
out duration

out maximum(duration) as max
out average(duration) as avg
```

# Checking Correctness of Values

```
#include <stdio.h>
#include <unistd.h>

int add(int a, int b) {
    return a + b;
}

int main() {
    printf("%i\n", add(2,3));
    printf("%i\n", add(17,4));
    printf("%i\n", add(2000000000,1000000000));
}
```

```
@InstFunctionCallArg("add", 0)
in a: Events[Int]
@InstFunctionCallArg("add", 1)
in b: Events[Int]

@InstFunctionReturnValue("add")
in r: Events[Int]

def should = last(a + b, r)
def ok = r == should

out a
out b
out r
out should
out ok
```

# Multiple Threads

```
#include <pthread.h>

void foo() {}

void *task () {
    foo();
    foo();
    foo();
    return NULL;
}

int main ()
{
    pthread_t t1, t2;
    pthread_create(&t1, NULL, &task, NULL);
    pthread_create(&t2, NULL, &task, NULL);

    pthread_join(t1, NULL);
    pthread_join(t2, NULL);

    return 0;
}
```

```
@InstFunctionCall("foo")
in foo: Events[Unit]

@ThreadId
in tid: Events[Int]

out foo
out tid
```

# Checking Correct Locking

```
#include <pthread.h>
#include <unistd.h>

int shared_memory[4] = {0};
pthread_mutex_t locks[4] = {
    PTHREAD_MUTEX_INITIALIZER, PTHREAD_MUTEX_INITIALIZER,
    PTHREAD_MUTEX_INITIALIZER, PTHREAD_MUTEX_INITIALIZER,
};

void use(int index) {
    shared_memory[index]++;
}

void *task1 () {
    for (int i = 0; i < 4; i++) {
        pthread_mutex_lock(&locks[i]);
        use(i);
        pthread_mutex_unlock(&locks[i]);
    }
    return NULL;
}
```

```
void *task2 () {
    for (int i = 3; i >= 0; i--) {
        pthread_mutex_lock(&locks[i]);
        use(i);
        pthread_mutex_unlock(&locks[i]);
    }
    return NULL;
}

int main ()
{
    pthread_t t1, t2;
    pthread_create(&t1, NULL, &task1, NULL);
    pthread_create(&t2, NULL, &task2, NULL);

    pthread_join(t1, NULL);
    pthread_join(t2, NULL);

    return 0;
}
```



# Checking Correct Locking (2)

```
@InstFunctionCallArg("pthread_mutex_lock", 0)
in lock: Events[Int]

@InstFunctionCallArg("pthread_mutex_unlock", 0)
in release: Events[Int]

@InstFunctionCallArg("use", 0)
in access: Events[Int]

@ThreadId
in tid: Events[Int]

def locksOfThread = {
  def oldMap = last(map, tid)
  def oldLocks = Map.getOrElse(oldMap, tid, Set.empty[Int])
  def map: Events[Map[Int, Set[Int]]] = merge3(
    on(lock, Map.add(oldMap, tid, Set.add(oldLocks, lock))),
    on(release, Map.add(oldMap, tid, Set.remove(oldLocks, release))),
    Map.empty[Int, Set[Int]])
  map
}
```

```
def locksForResource = {
  def old = last(map, access)
  def currentLocks = Map.get(locksOfThread, tid)
  def map: Events[Map[Int, Set[Int]]] = merge(
    on(access, Map.add(old, access, Set.intersection(
      currentLocks,
      Map.getOrElse(old, access, currentLocks)))),
    Map.empty[Int, Set[Int]])
  map
}

def error = unitIf(Set.size(Map.get(locksForResource, access)) == 0)
out error
```

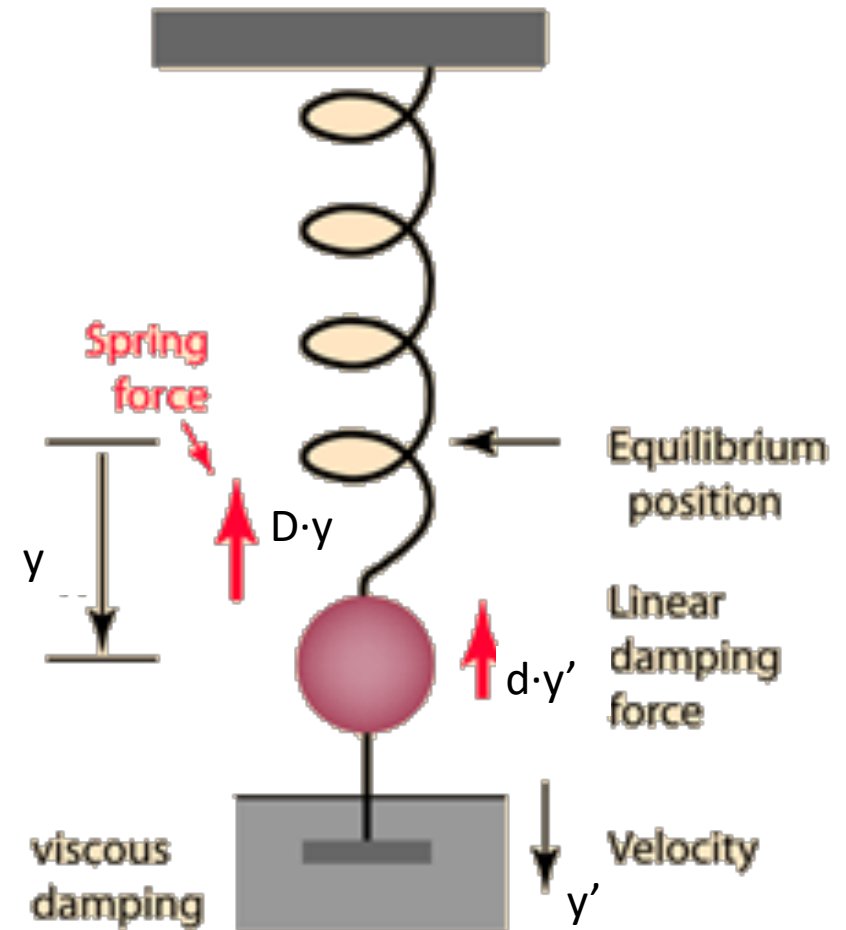
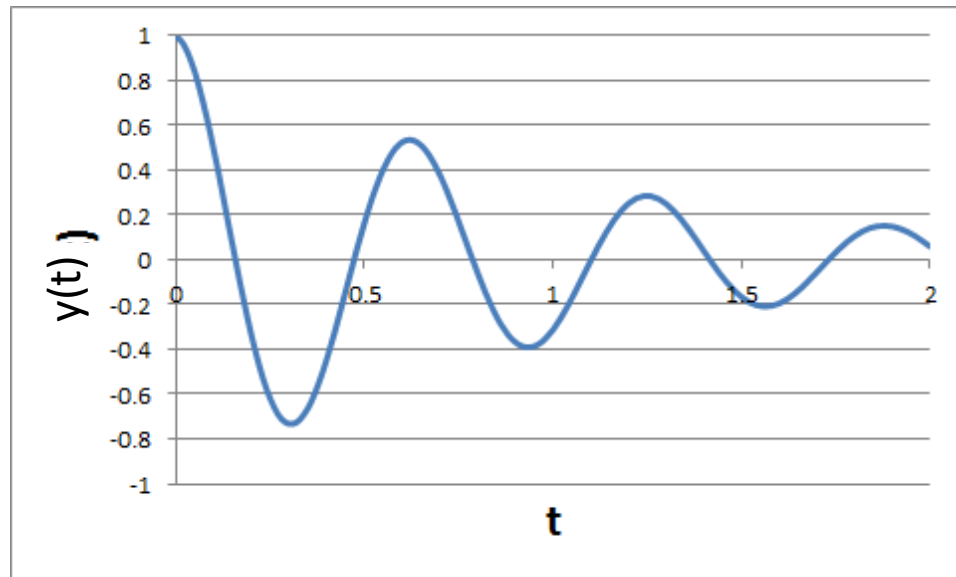
# Cyber-Physical Systems

# Cyber-Physical System

- Communicating hybrid systems
- Communicating embedded systems interacting with the physical world
  
- Discrete Math, Events, Propositions
- Continuous Math, Signals

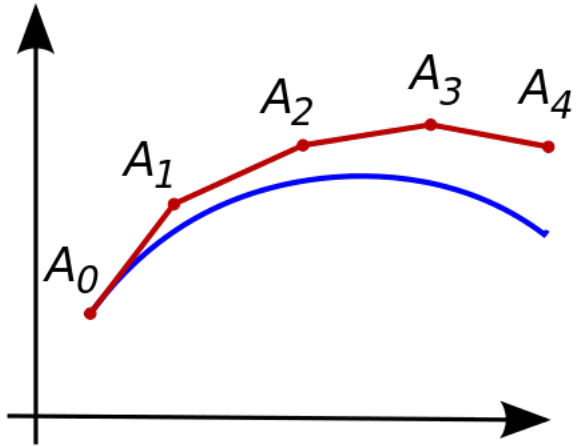
# Damped Harmonic Oscillator

$$m \cdot y'' = -D \cdot y - d \cdot y'$$

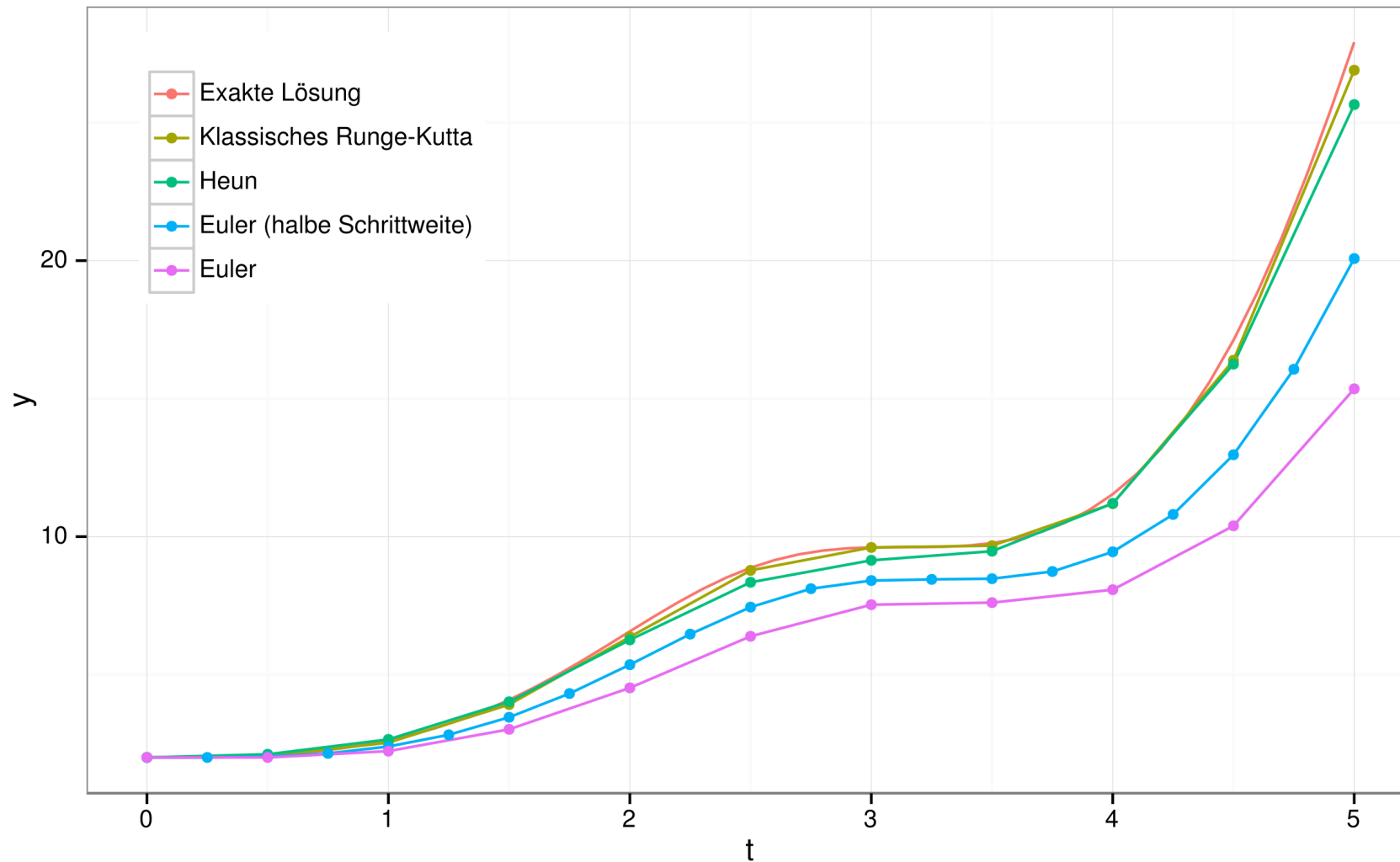


# Solving of ODE – Numerical Approximations

- Euler's method



# Solving of ODE – A Variety of Methods

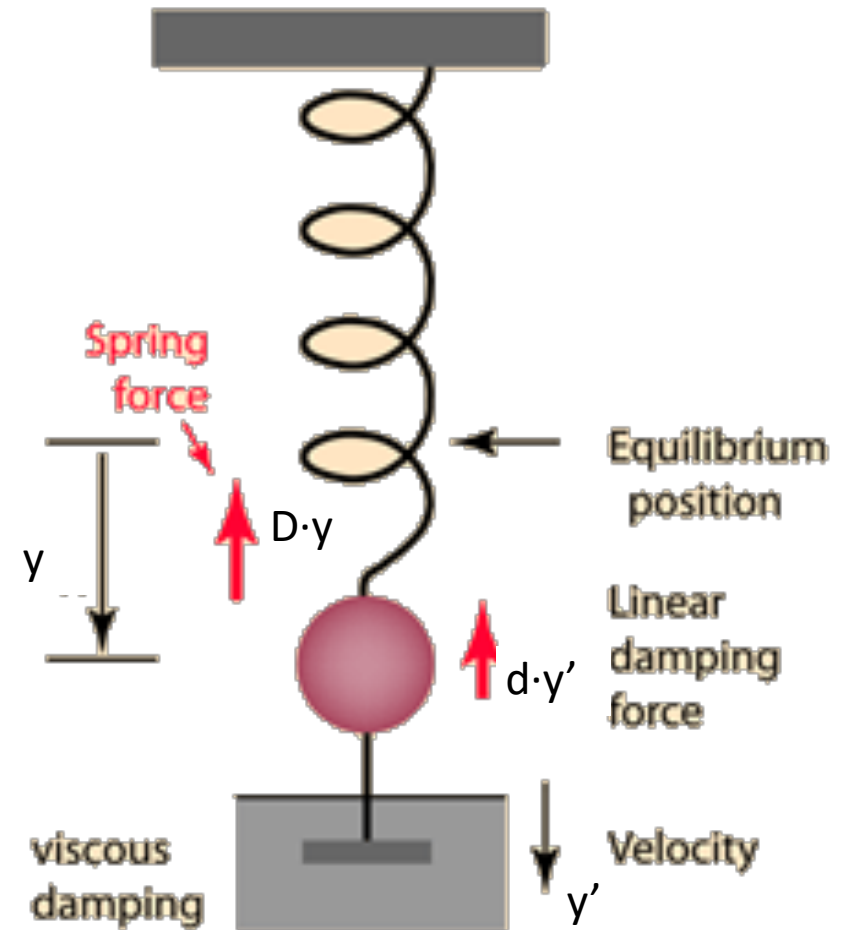
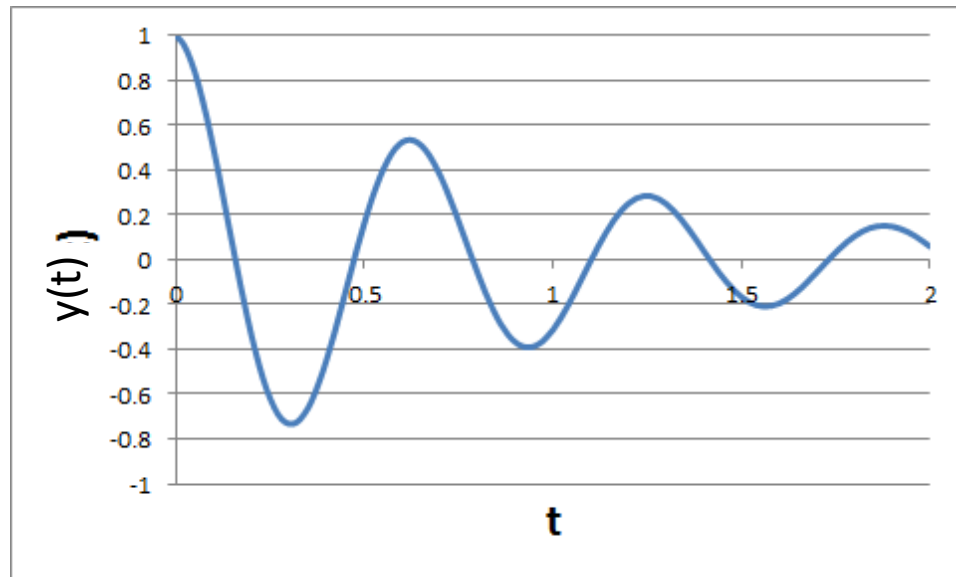


By Svchbderivative work:  
tobi (talk) - RK Verfahren,  
CC BY-SA 3.0, <https://commons.wikimedia.org/w/index.php?curid=32717385>

# ODEs in TeSSLa

# Damped Harmonic Oscillator

$$m \cdot y'' = -D \cdot y - d \cdot y'$$

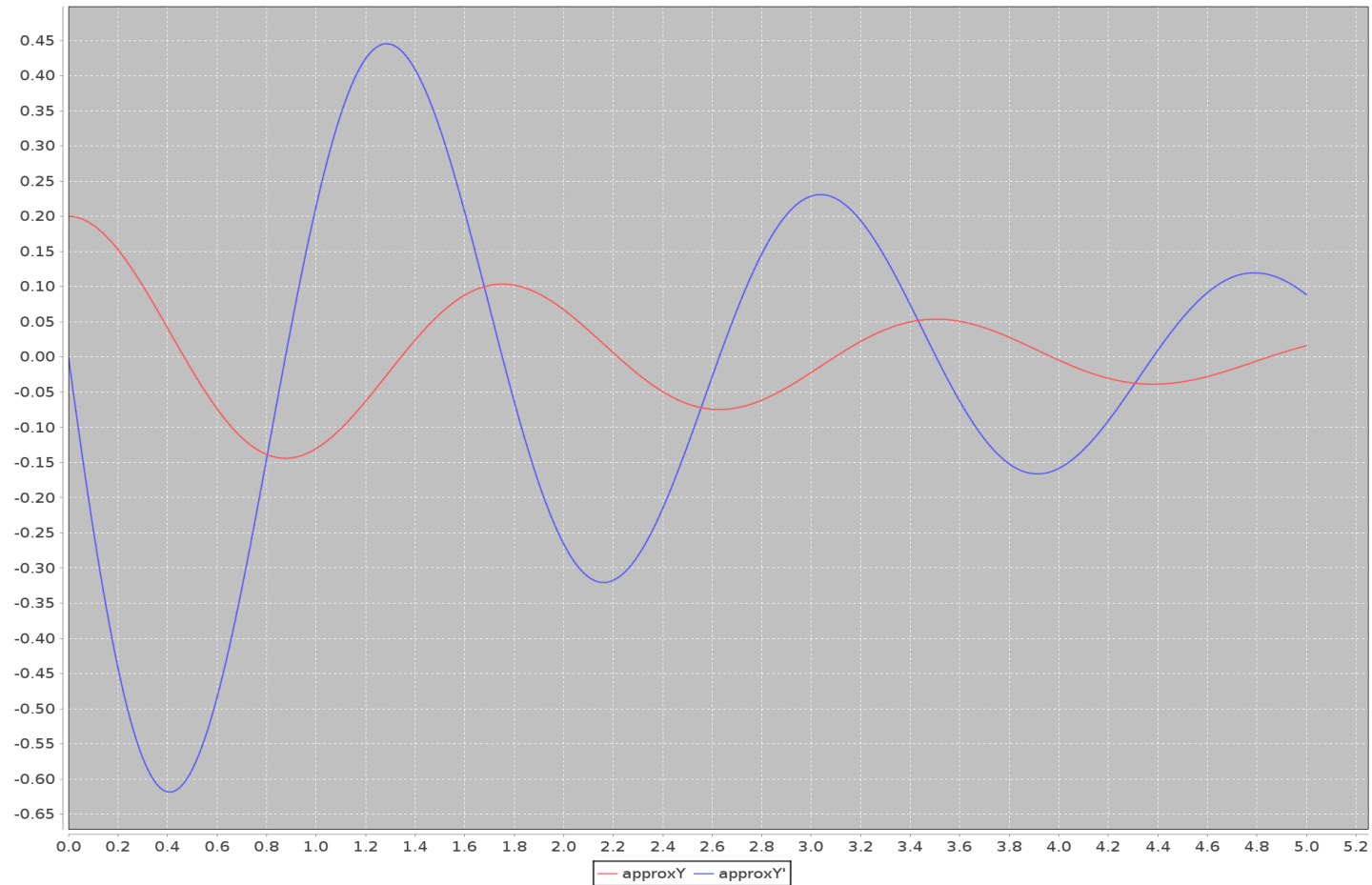




# The Spring Example

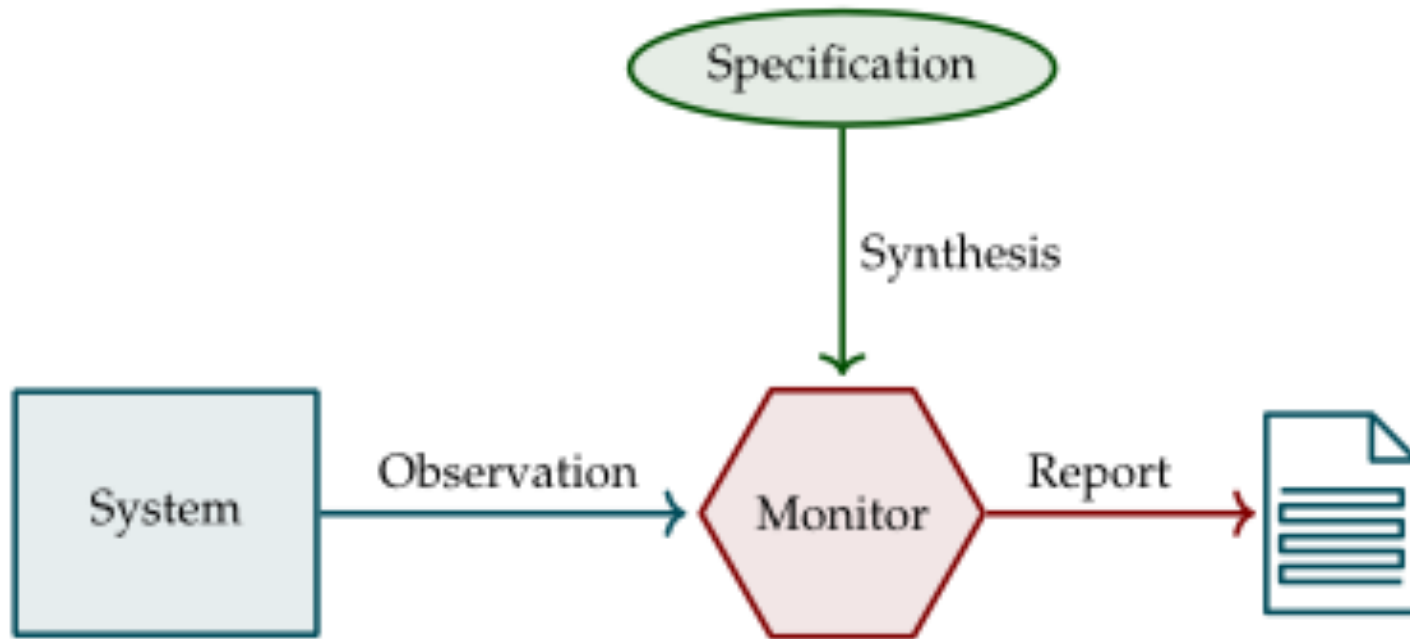
```
1 in sensor: Events[Float]
2
3 def m: Float = 0.2      # kg
4 def D: Float = 2.6     # N/m
5 def d: Float = 0.15    # kg/s
6 def y''(t: Float, y: Float, y': Float): Float =
7   -D / m * y - d / m * y'
8 def y_0 = 0.2          # m
9 def y'_0 = 0.0         # m/s
10
11 def approx: Events[(Float, Float)] = rk4(y'', y_0, y'_0)
12 def approxY: Events[Float] = approx._1
13 def approxY': Events[Float] = approx._2
14 def alarm = |sensor - approxY| > ε
```

# Plot of the Damped Spring



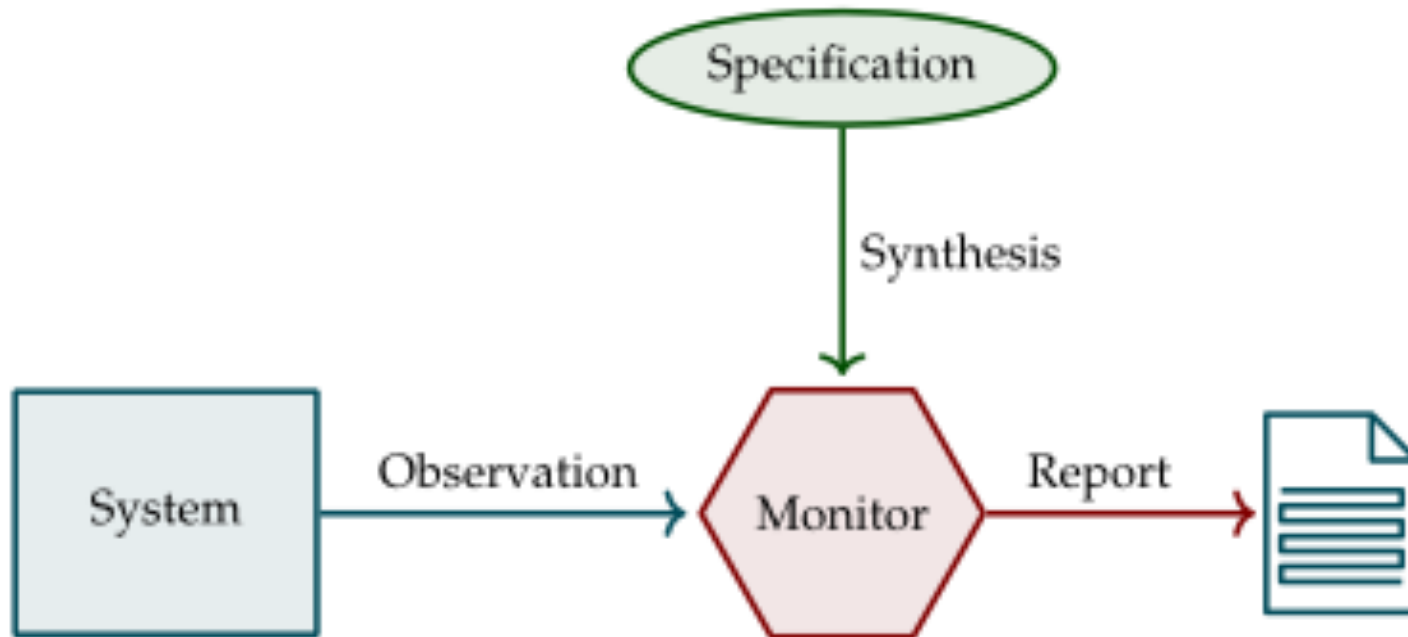
Control

# Runtime Verification

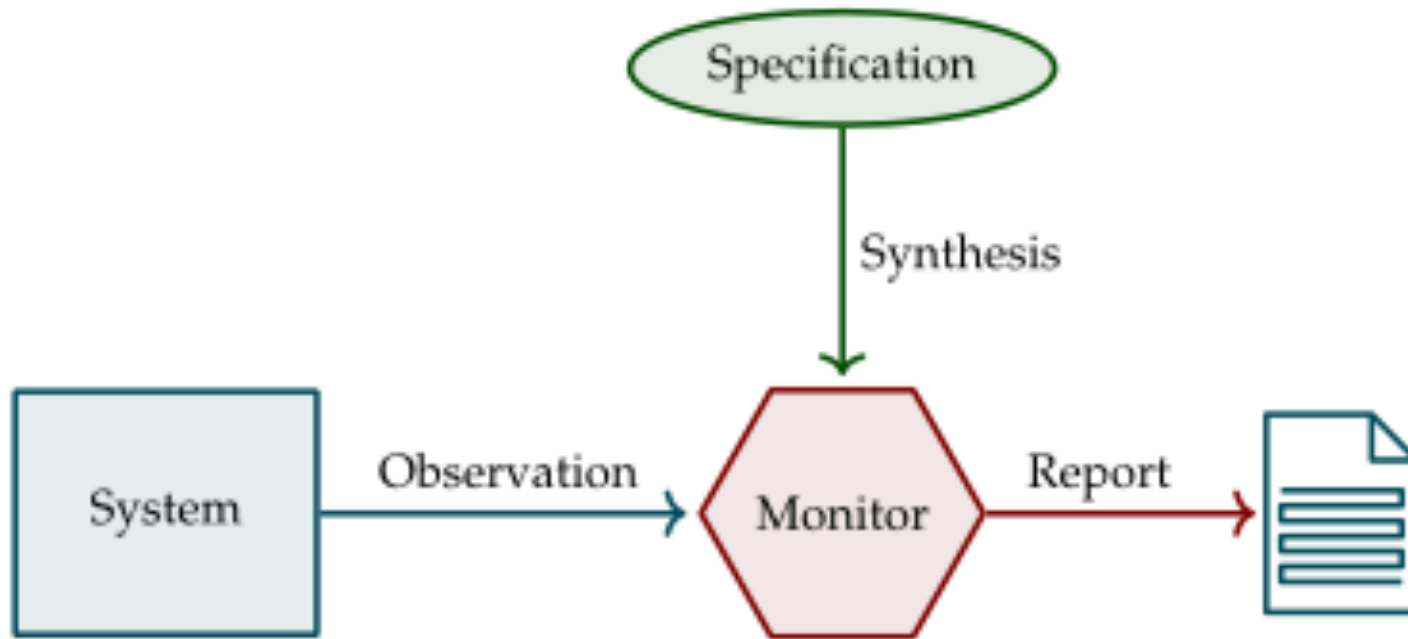


# Runtime Verification

- Partial Verification

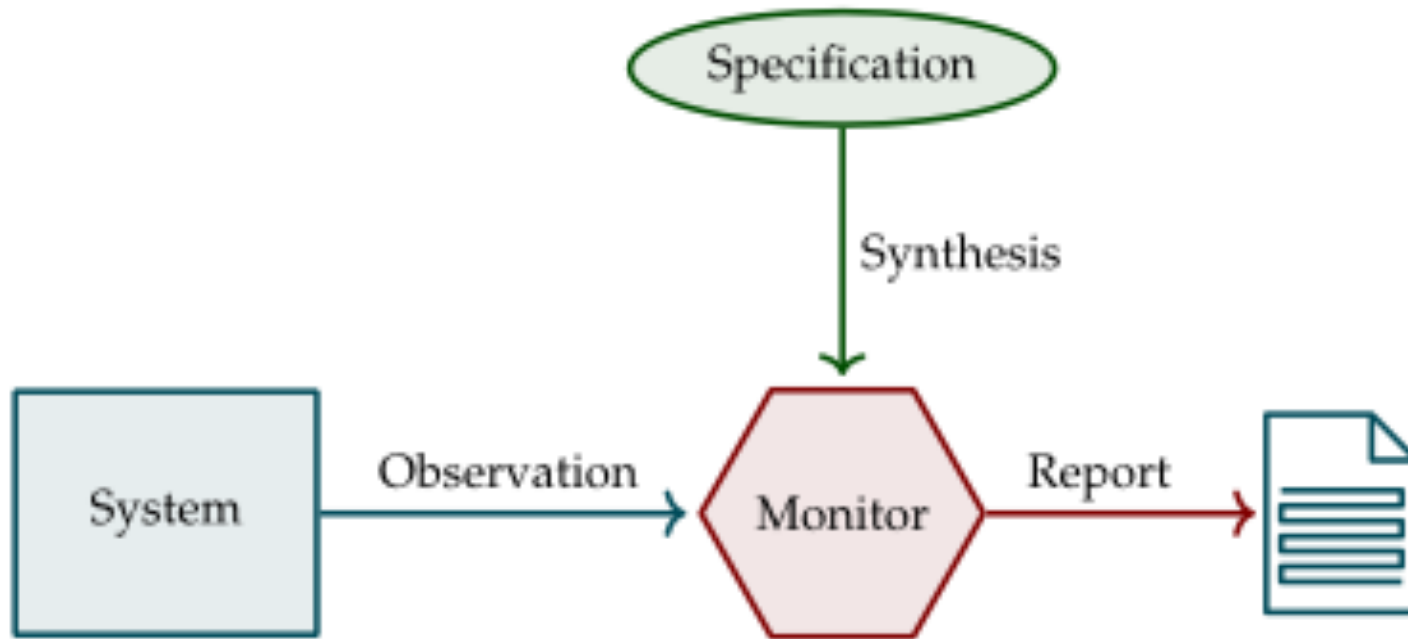


# Runtime Verification



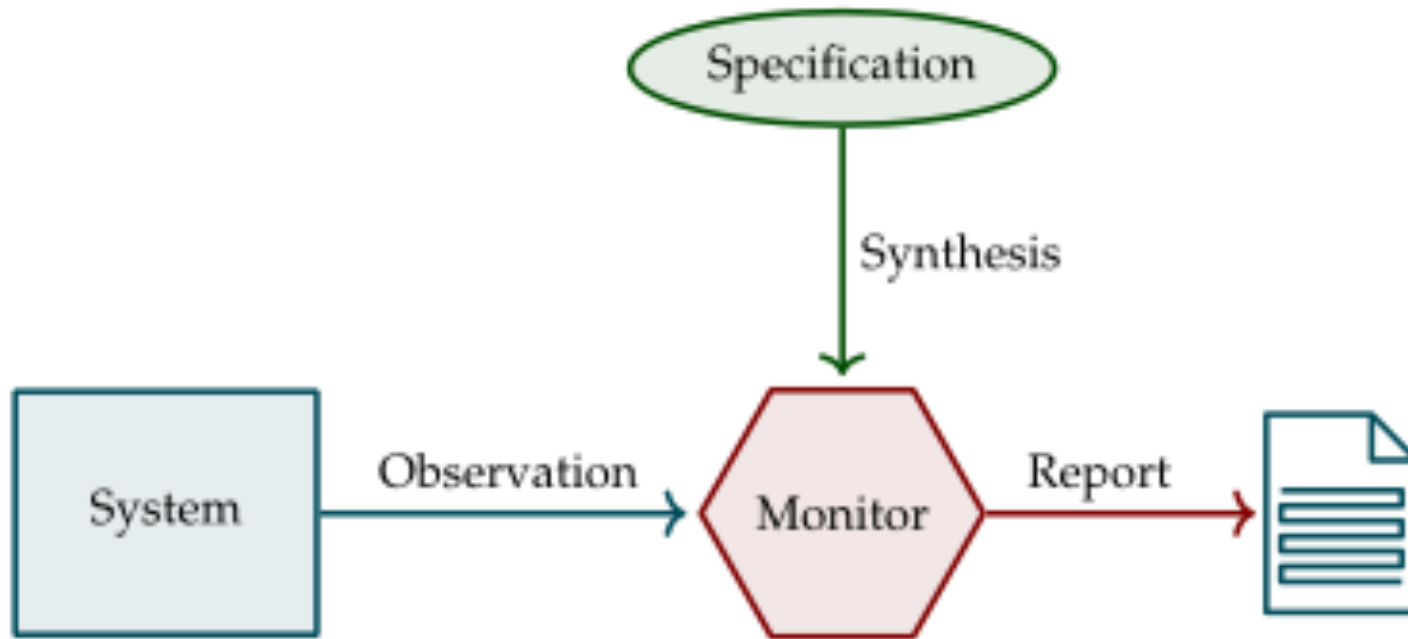
- Partial Verification
- Testing Temporal Assertions

# Runtime Verification



- Partial Verification
- Testing Temporal Assertions
- Test Cases as Input Sequences checked by Monitors

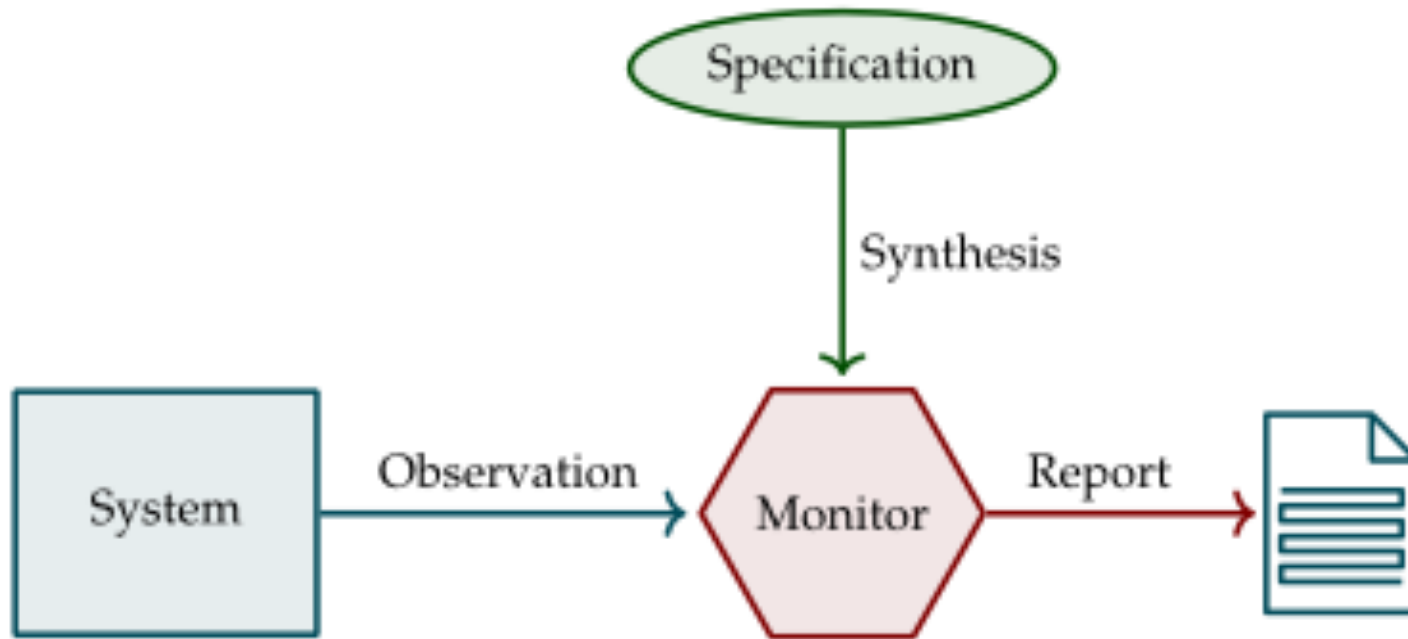
# Runtime Verification



- Partial Verification
- Testing Temporal Assertions
- Test Cases as Input Sequences checked by Monitors
- Debugging

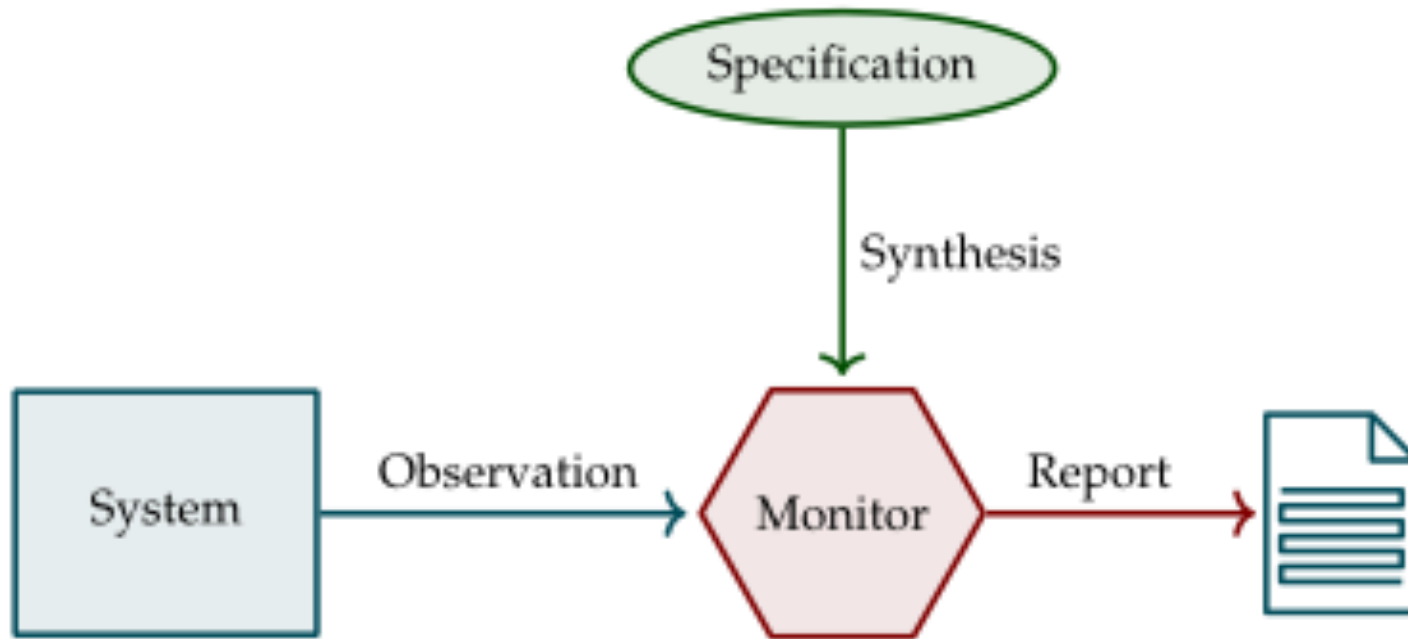


# Runtime Verification



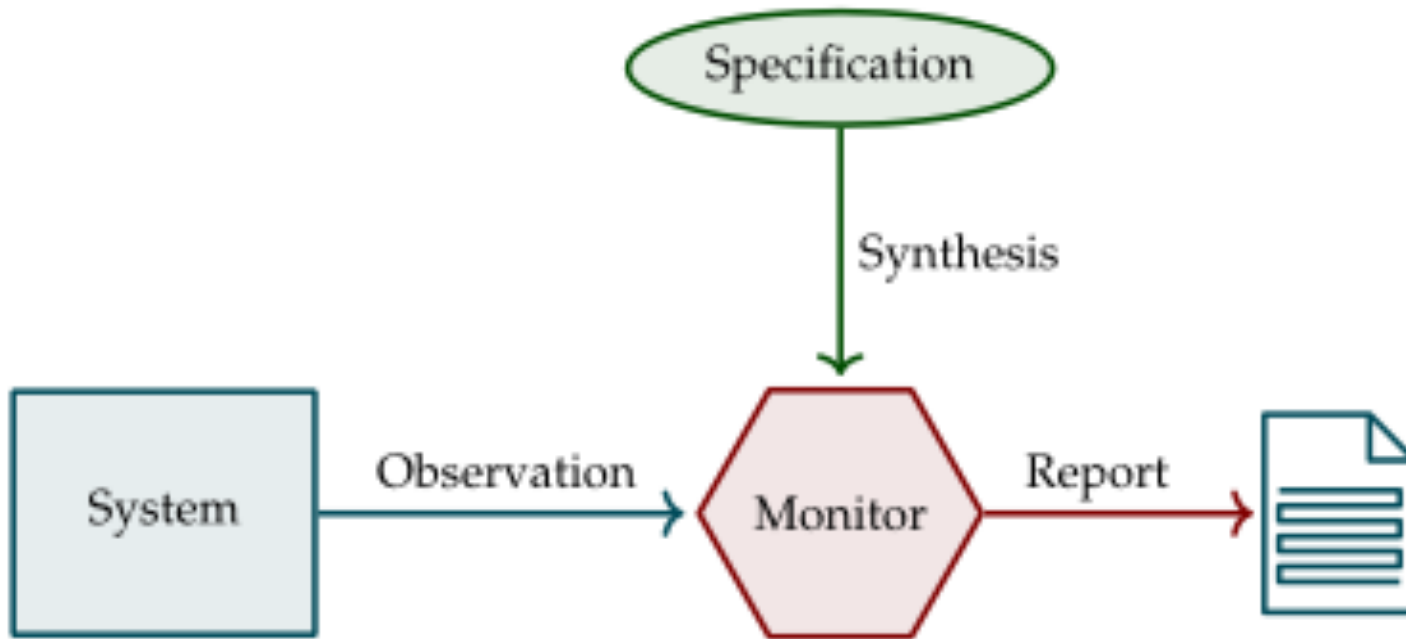
- Partial Verification
- Testing Temporal Assertions
- Test Cases as Input Sequences checked by Monitors
- Debugging
- Control?

# Control from an RV Point of View

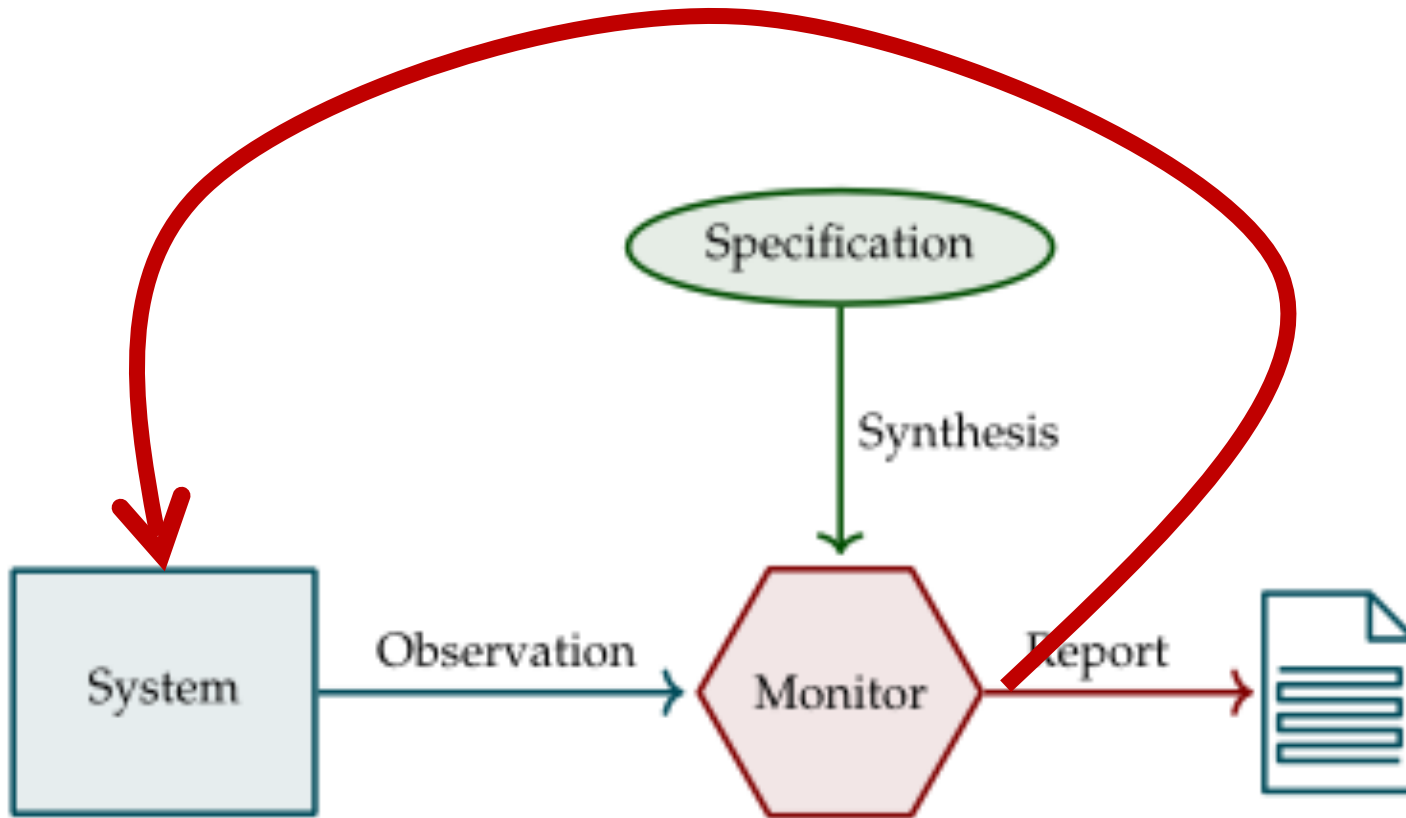


# Control from an RV Point of View

- Monitor Output as Feedback/ Intervention to System

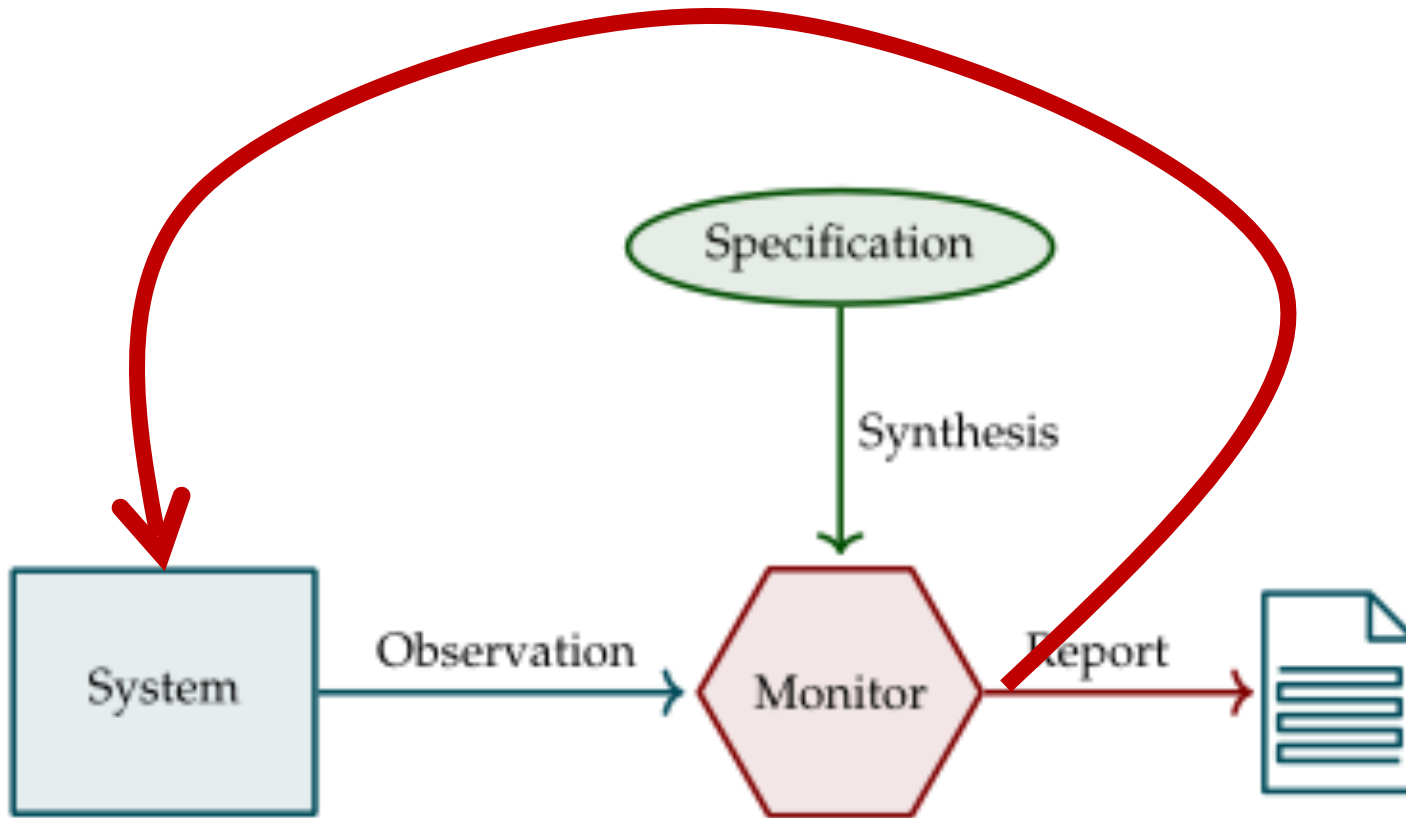


# Control from an RV Point of View



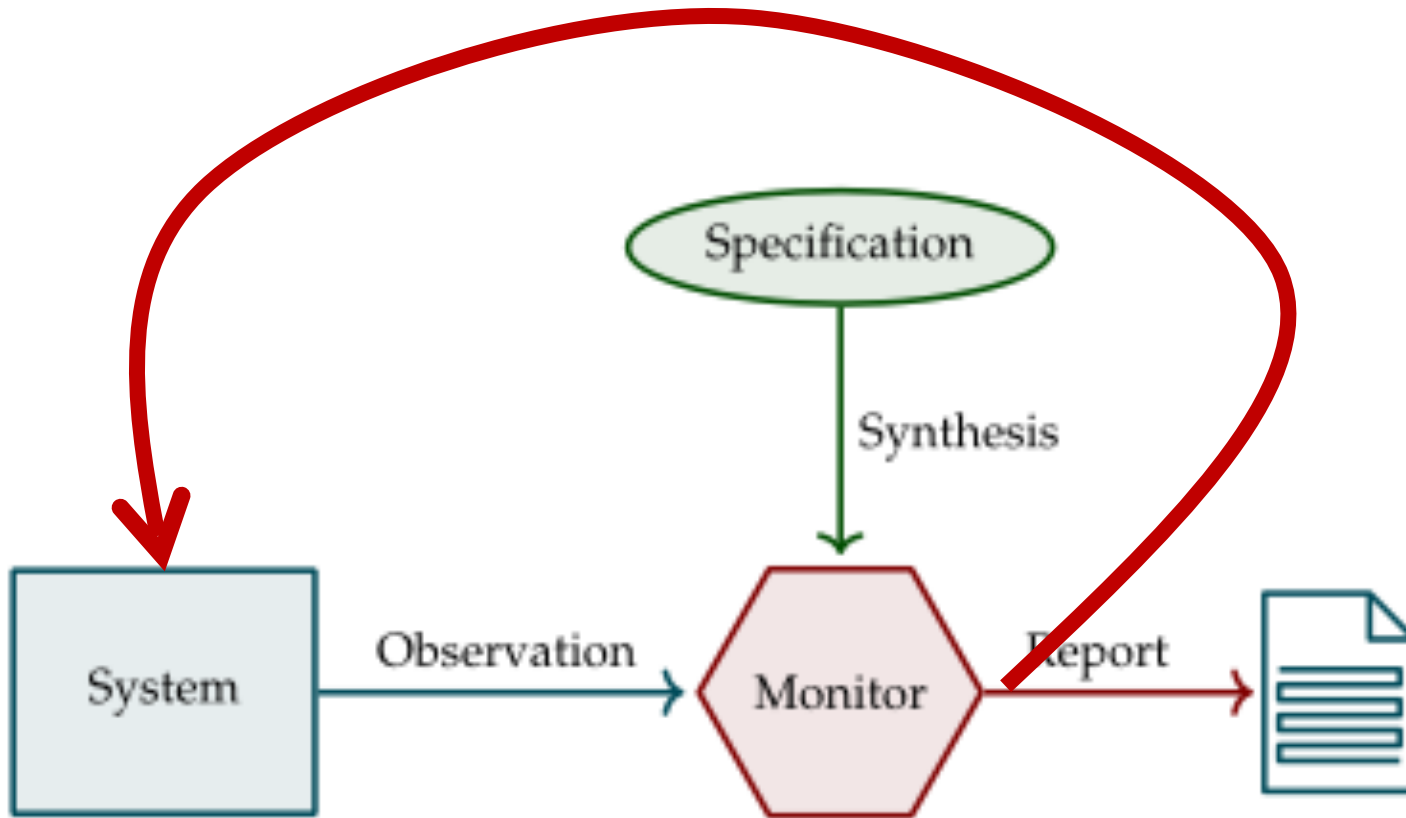
- Monitor Output as Feedback/ Intervention to System

# Control from an RV Point of View



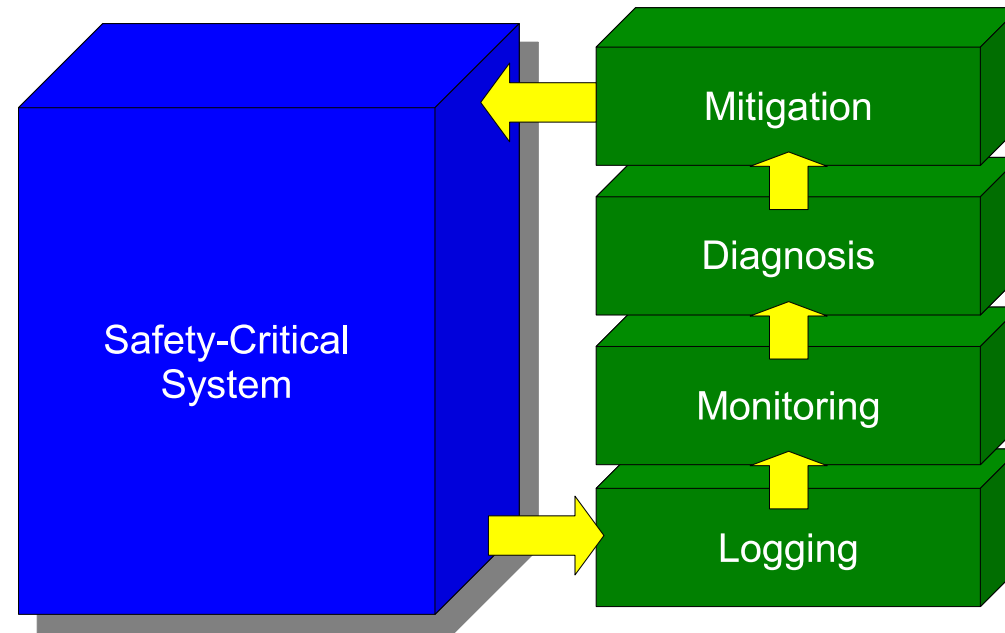
- Monitor Output as Feedback/ Intervention to System
- Monitor has to give more specific Output

# Control from an RV Point of View

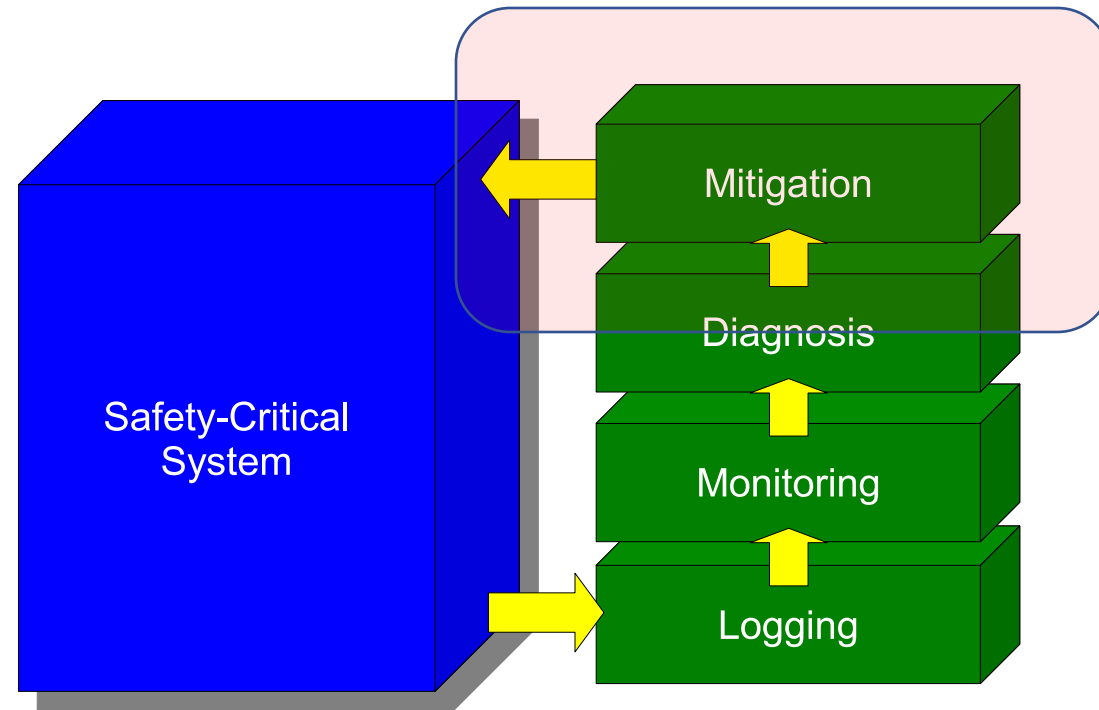


- Monitor Output as Feedback/ Intervention to System
- Monitor has to give more specific Output
- Here: Monitor actually computes control values

# Self-Healing System (FDIR with RV)

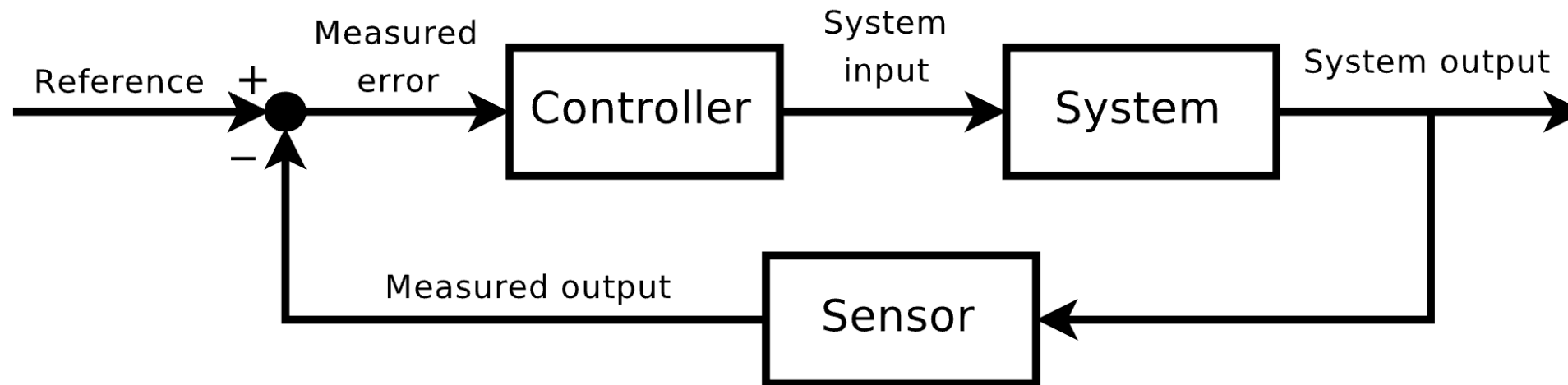


# Self-Healing System (FDIR with RV)



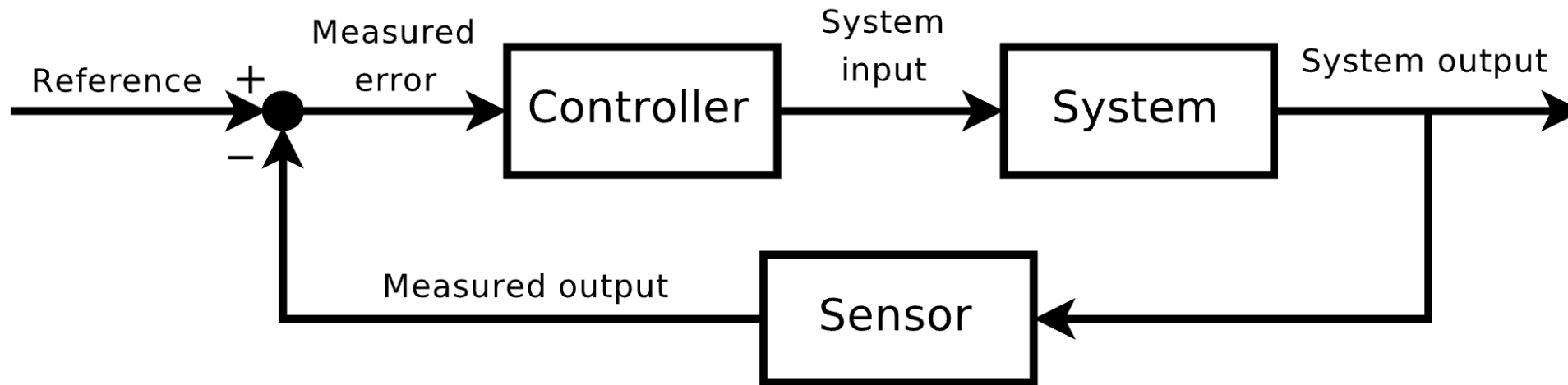


# Control



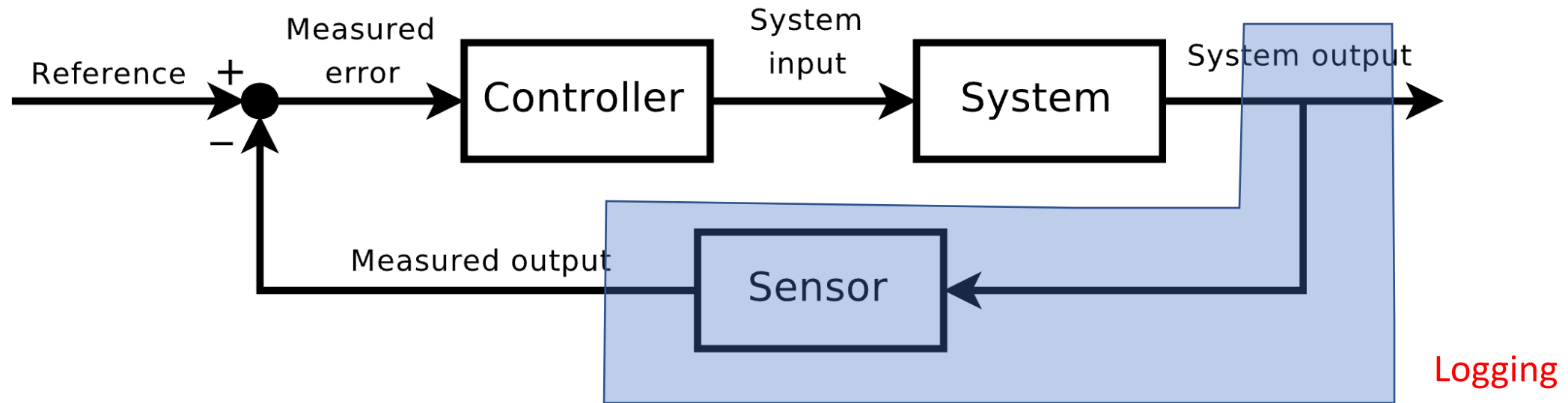
# Control

## Closed-Loop Controller - Feedback



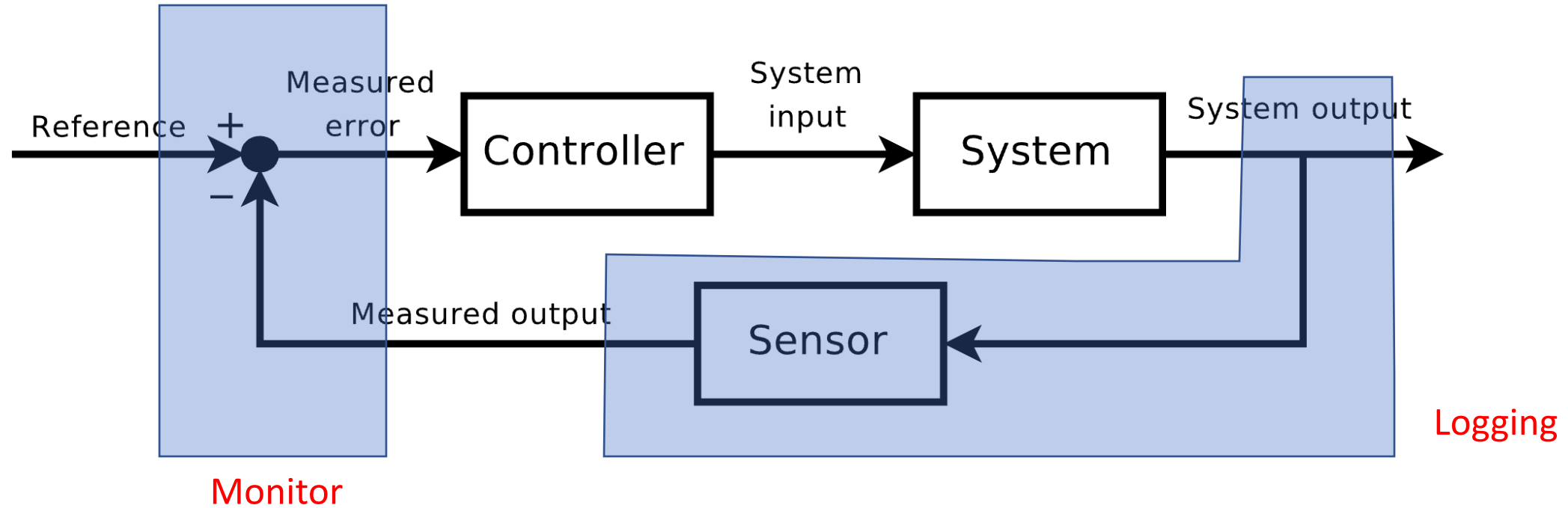
# Control

## Closed-Loop Controller - Feedback



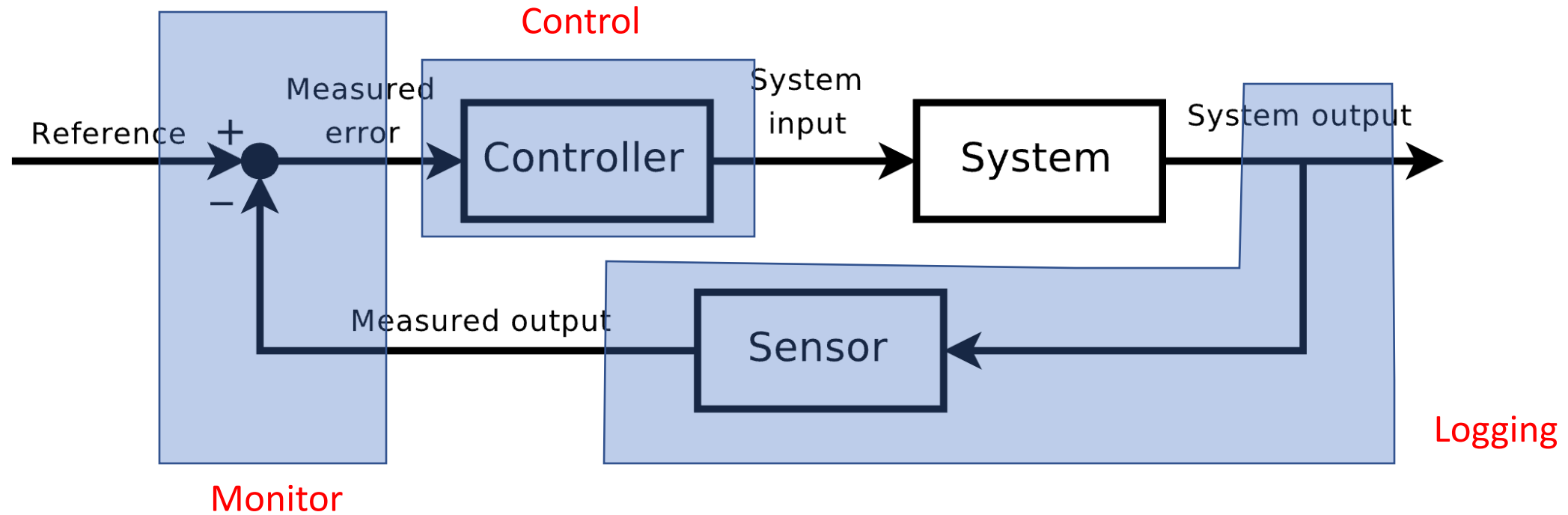
# Control

## Closed-Loop Controller - Feedback

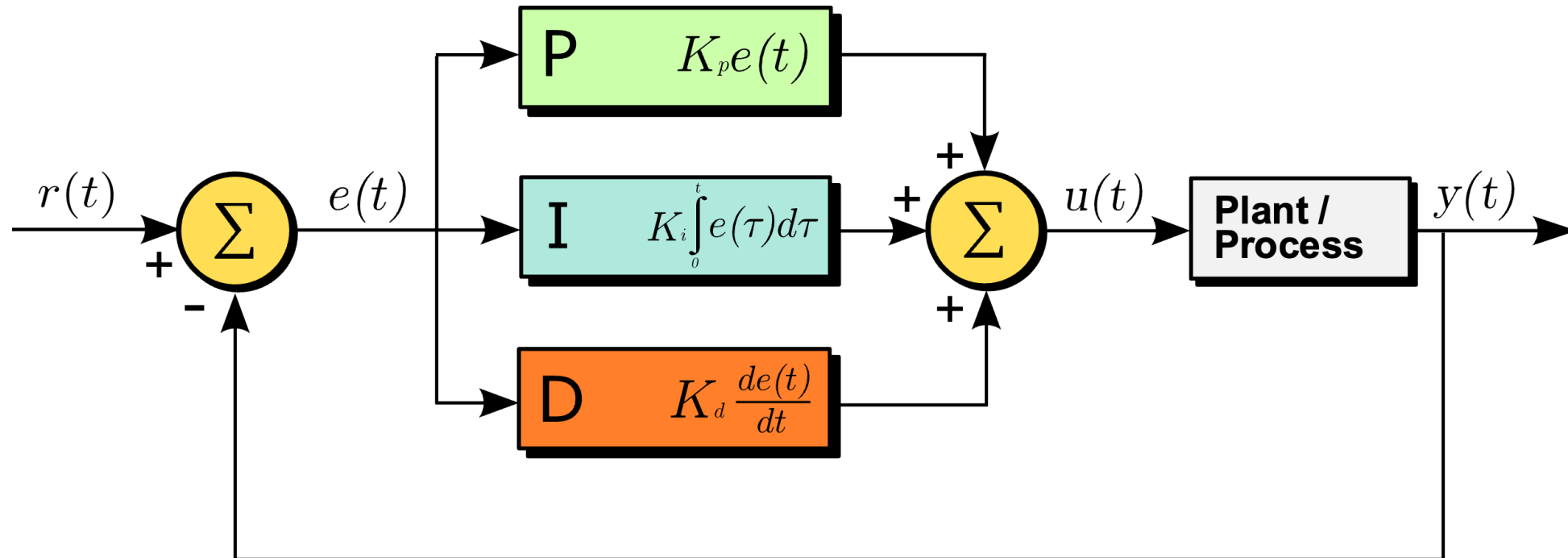


# Control

## Closed-Loop Controller - Feedback



# PID-Controller



By Arturo Urquizo - <http://commons.wikimedia.org/wiki/File:PID.svg>, CC BY-SA 3.0, <https://commons.wikimedia.org/w/index.php?curid=17633925>

# Controller Combinations

**P**

**Proportional** controller to reduce the transient period.  
*Changes the magnitude only.*

**I**

**Integral** controller to reduce the time invariant error  
*Lags the output phase.*

**D**

**Derivative** controller to minimize the transient errors like overshoot, oscillatory response.  
*Leads the output phase.*

**PI**

Reduces rise time and steady state errors  
*Changes the magnitude as well as lags the output.*

**PD**

Reduces rise time and transient errors such as overshoot, oscillations in output.  
*Changes both the magnitude as well as adds a leading phase to the output.*

**PID**

General case of a controller. Can be used to control the magnitude and lead/ lag phase problems.  
*Changes the magnitude and can add positive or negative phase to the output as per the requirements.*

# Code of Controller in TeSSLa

- *See* [tessla.io](http://tessla.io)



# Controlling Robots

# TeSSLa/ROS Bridge

```
include "TesslaROSBridge.tessla"  
  
@RosSubscription("/reduced_scan_to_tessla", "int64", "10")  
in scan: Events[Int]  
  
# Stop if there are short rays detected  
  
def stop = scan < 20  
  
@RosPublisher("/result_from_tessla_to_ros", "bool", "10")  
out stop
```

# Example

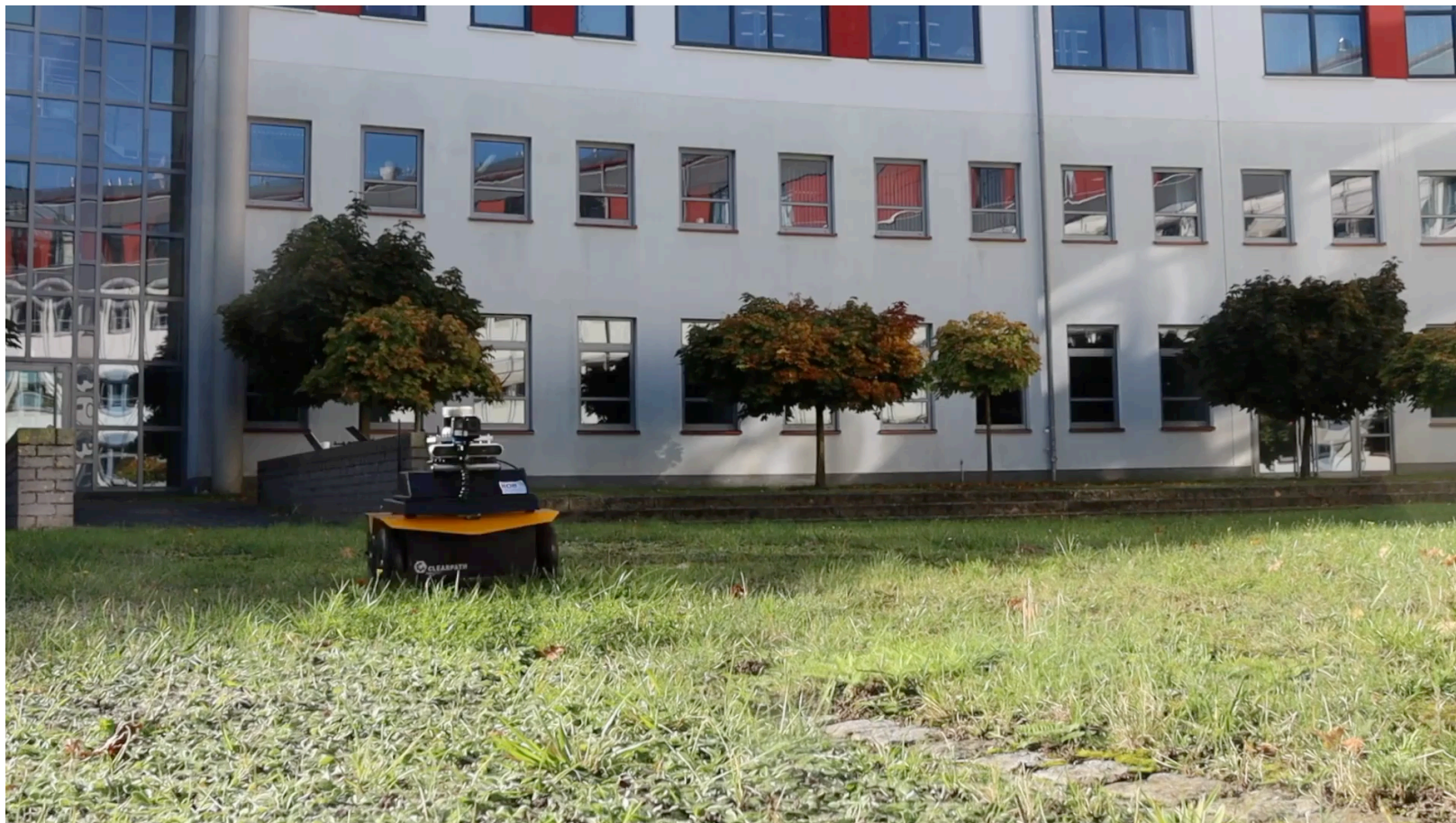


# Example





# Example



# Conclusions

# Conclusions

- Stream-based Runtime Verification makes sense
- TeSSLa one approach in this setting
- Supports handling of data
  
- Monitoring CPS makes sense
- Controlling using RV techniques makes sense
- Separation of concerns

# Future Work

- Controller module in TeSSLa?
- More concrete examples?
- Gain more experiences?
- Programming (safety aspects) of robots?
- Better use Modelica and FMUs?
- Add continuous functions symbolically to perform algebraic simplifications?