# Models of Computation 1

Mayank Goswami

23.06.2014

Mayank Goswami Models of Computation 1

< 17 b

< ∃⇒

- Comparison trees
- Linear decision trees.
- Algebraic decision trees.
- Today: Algebraic computation trees, Ben-Or's theorem.

- Comparison trees
- Linear decision trees.
- Algebraic decision trees.
- Today: Algebraic computation trees, Ben-Or's theorem.
- **Today:** Models of computations part 1: Word-RAM, pointer machine and indexability.

### Problems we considered last time

- Sorting: Given a sequence {x<sub>1</sub>, · · · , x<sub>n</sub>} of n distinct numbers, find the permutation π such that x<sub>π(1)</sub> < x<sub>π(2)</sub> < · · · < x<sub>π(n)</sub>.
- **Convex hull**: Given a set of points  $\{p_1, \dots, p_n\}$  in the plane, compute their convex hull.
- Compute the maximum element in a sequence.
- **Element uniqueness**: Are any two elements of the input set  $\{x_1, \dots, x_n\}$  equal?
- Set intersection: Given two sets  $\{x_1, \dots, x_n\}$  and  $\{y_1, \dots, y_n\}$ , do they intersect?
- **3SUM**: Do any three elements in the input set {*x*<sub>1</sub>, · · · , *x<sub>n</sub>*} sum to zero?

(1日) (1日) (日)

In general, the closer a model is to a real computer, the harder it is to prove lower bounds in that model.

▶ < ≣ ▶

In general, the closer a model is to a real computer, the harder it is to prove lower bounds in that model. The computational model most geared towards designing algorithms is the unit cost word-RAM.

In general, the closer a model is to a real computer, the harder it is to prove lower bounds in that model. The computational model most geared towards designing algorithms is the unit cost word-RAM.

 Data structure is represented as a RAM, partitioned into words of w bits each; the words have integer addresses amongst {0, · · · , 2<sup>w</sup> − 1}(can store pointers).

In general, the closer a model is to a real computer, the harder it is to prove lower bounds in that model. The computational model most geared towards designing algorithms is the unit cost word-RAM.

- Data structure is represented as a RAM, partitioned into words of w bits each; the words have integer addresses amongst {0, · · · , 2<sup>w</sup> − 1}(can store pointers).
- Retrieving a word takes constant time.

In general, the closer a model is to a real computer, the harder it is to prove lower bounds in that model. The computational model most geared towards designing algorithms is the unit cost word-RAM.

- Data structure is represented as a RAM, partitioned into words of w bits each; the words have integer addresses amongst {0, · · · , 2<sup>w</sup> − 1}(can store pointers).
- Retrieving a word takes constant time.
- Any "reasonable" operation (integer addition/division, bitwise AND, OR, XOR, left/right shifts, comparisons) on words takes constant time.

通 とう ほとう きょう

In general, the closer a model is to a real computer, the harder it is to prove lower bounds in that model. The computational model most geared towards designing algorithms is the unit cost word-RAM.

- Data structure is represented as a RAM, partitioned into words of w bits each; the words have integer addresses amongst {0, · · · , 2<sup>w</sup> − 1}(can store pointers).
- Retrieving a word takes constant time.
- Any "reasonable" operation (integer addition/division, bitwise AND, OR, XOR, left/right shifts, comparisons) on words takes constant time.
- w = Ω(log n), where n is the size of the input allows for storing an index into the data structure in a single word.

▲圖 ▶ ▲ 臣 ▶ ▲ 臣 ▶ …

In general, the closer a model is to a real computer, the harder it is to prove lower bounds in that model. The computational model most geared towards designing algorithms is the unit cost word-RAM.

- Data structure is represented as a RAM, partitioned into words of w bits each; the words have integer addresses amongst {0, · · · , 2<sup>w</sup> − 1}(can store pointers).
- Retrieving a word takes constant time.
- Any "reasonable" operation (integer addition/division, bitwise AND, OR, XOR, left/right shifts, comparisons) on words takes constant time.
- w = Ω(log n), where n is the size of the input allows for storing an index into the data structure in a single word.
- **Query time:** Number of instructions needed to answer a query.

・ 回 ト ・ ヨ ト ・ ヨ ト

In general, the closer a model is to a real computer, the harder it is to prove lower bounds in that model. The computational model most geared towards designing algorithms is the unit cost word-RAM.

- Data structure is represented as a RAM, partitioned into words of w bits each; the words have integer addresses amongst {0, · · · , 2<sup>w</sup> − 1}(can store pointers).
- Retrieving a word takes constant time.
- Any "reasonable" operation (integer addition/division, bitwise AND, OR, XOR, left/right shifts, comparisons) on words takes constant time.
- w = Ω(log n), where n is the size of the input allows for storing an index into the data structure in a single word.
- **Query time:** Number of instructions needed to answer a query.
- Space usage: Largest address used.

イロン イロン イヨン イヨン

In general, the closer a model is to a real computer, the harder it is to prove lower bounds in that model. The computational model most geared towards designing algorithms is the unit cost word-RAM.

- Data structure is represented as a RAM, partitioned into words of w bits each; the words have integer addresses amongst {0, · · · , 2<sup>w</sup> − 1}(can store pointers).
- Retrieving a word takes constant time.
- Any "reasonable" operation (integer addition/division, bitwise AND, OR, XOR, left/right shifts, comparisons) on words takes constant time.
- w = Ω(log n), where n is the size of the input allows for storing an index into the data structure in a single word.
- **Query time:** Number of instructions needed to answer a query.
- Space usage: Largest address used.

イロン イロン イヨン イヨン

We do not want lower bounds to depend on the exact set of machine instructions available.

臣

We do not want lower bounds to depend on the exact set of machine instructions available. The cell-probe model is a less restrictive version of word-RAM:

• Memory of cells, each w bits long, addresses in

 $\{0,\cdots,2^w-1\}.$ 

▶ < 문 ▶ < 문 ▶</p>

We do not want lower bounds to depend on the exact set of machine instructions available. The cell-probe model is a less restrictive version of word-RAM:

- Memory of cells, each w bits long, addresses in  $\{0,\cdots,2^w-1\}.$
- We say data structure uses S cells of space if only cells of addresses  $\{0, \cdots, S-1\}$  are used.

We do not want lower bounds to depend on the exact set of machine instructions available. The cell-probe model is a less restrictive version of word-RAM:

- Memory of cells, each w bits long, addresses in  $\{0, \cdots, 2^w 1\}$ .
- We say data structure uses S cells of space if only cells of addresses  $\{0, \cdots, S-1\}$  are used.
- Given a query, the data structure probes a number of cells from memory, and at the end must answer the query.

We do not want lower bounds to depend on the exact set of machine instructions available. The cell-probe model is a less restrictive version of word-RAM:

- Memory of cells, each w bits long, addresses in  $\{0, \cdots, 2^w 1\}$ .
- We say data structure uses S cells of space if only cells of addresses  $\{0, \cdots, S-1\}$  are used.
- Given a query, the data structure probes a number of cells from memory, and at the end must answer the query.
- The cell probed at the next step may be any deterministic function of the query and the contents of the previously probed cells.

・ 回 ト ・ ヨ ト ・ ヨ ト …

We do not want lower bounds to depend on the exact set of machine instructions available. The cell-probe model is a less restrictive version of word-RAM:

- Memory of cells, each w bits long, addresses in  $\{0, \cdots, 2^w 1\}$ .
- We say data structure uses S cells of space if only cells of addresses  $\{0, \cdots, S-1\}$  are used.
- Given a query, the data structure probes a number of cells from memory, and at the end must answer the query.
- The cell probed at the next step may be any deterministic function of the query and the contents of the previously probed cells.
- Thus, all computations on read data are free of charge; arbitrary instructions allowed.

・ 回 ト ・ ヨ ト ・ ヨ ト …

We do not want lower bounds to depend on the exact set of machine instructions available. The cell-probe model is a less restrictive version of word-RAM:

- Memory of cells, each w bits long, addresses in  $\{0, \cdots, 2^w 1\}$ .
- We say data structure uses S cells of space if only cells of addresses  $\{0, \cdots, S-1\}$  are used.
- Given a query, the data structure probes a number of cells from memory, and at the end must answer the query.
- The cell probed at the next step may be any deterministic function of the query and the contents of the previously probed cells.
- Thus, all computations on read data are free of charge; arbitrary instructions allowed.
- Query cost: Number of cells probed.

回 とうほう うほとう

We do not want lower bounds to depend on the exact set of machine instructions available. The cell-probe model is a less restrictive version of word-RAM:

- Memory of cells, each w bits long, addresses in  $\{0, \cdots, 2^w 1\}$ .
- We say data structure uses S cells of space if only cells of addresses  $\{0, \cdots, S-1\}$  are used.
- Given a query, the data structure probes a number of cells from memory, and at the end must answer the query.
- The cell probed at the next step may be any deterministic function of the query and the contents of the previously probed cells.
- Thus, all computations on read data are free of charge; arbitrary instructions allowed.
- Query cost: Number of cells probed.
- Dynamic—**Update cost:** the number of cells read/written when performing an update

The cell probe model is the least restrictive of all lower bound models proposed in the literature. Thus, lower bounds proved here are very general.

→ < E → < E →</p>

The cell probe model is the least restrictive of all lower bound models proposed in the literature. Thus, lower bounds proved here are very general.Unfortunately,...

• • = • • = •

The cell probe model is the least restrictive of all lower bound models proposed in the literature. Thus, lower bounds proved here are very general.Unfortunately,...Today we will consider:

• Pointer machine model(Tarjan)

A B K A B K

The cell probe model is the least restrictive of all lower bound models proposed in the literature. Thus, lower bounds proved here are very general.Unfortunately,...Today we will consider:

- Pointer machine model(Tarjan)
- Indexability model(Papadimitrou et. al)

It is easier to prove high lower bounds in these models.

The cell probe model is the least restrictive of all lower bound models proposed in the literature. Thus, lower bounds proved here are very general.Unfortunately,...Today we will consider:

- Pointer machine model(Tarjan)
- Indexability model(Papadimitrou et. al)

It is easier to prove high lower bounds in these models. Finally we will consider the case when data is too large to fit into main memory. This is called the external memory model.

A B K A B K

The main problem we will be concerned is *orthogonal range reporting*:

- Input: A set of points  $P = \{p_1, \dots, p_n\}$  in the plane;  $p_i = (x_i, y_i)$ .
- **Preprocessing phase:** Data structure allowed some time(space) to preprocess *P* in order to answer...
- Query: Given a rectangle [x<sub>ℓ</sub>, x<sub>r</sub>] × [y<sub>ℓ</sub>, y<sub>r</sub>], report all points that lie inside this rectangle.

The main problem we will be concerned is *orthogonal range reporting*:

- Input: A set of points  $P = \{p_1, \dots, p_n\}$  in the plane;  $p_i = (x_i, y_i)$ .
- **Preprocessing phase:** Data structure allowed some time(space) to preprocess *P* in order to answer...
- Query: Given a rectangle [x<sub>ℓ</sub>, x<sub>r</sub>] × [y<sub>ℓ</sub>, y<sub>r</sub>], report all points that lie inside this rectangle.
- We study tradeoffs between space and query time. Usually space is O(poly(n)), and query is  $O(\text{poly}(\log n) + k)$ , where k is the set of output points.
- Problem can be generalized to  $\mathbb{R}^n$ .

・ 同 ト ・ ヨ ト ・ ヨ ト

• Filtering search

回 とくほとくほど

Filtering search

- Space  $O(n \log n / \log \log n)$ , query  $O(k + \log n)$ .
- No address manipulations are required.

向 ト イヨ ト イヨト

- Filtering search
  - Space  $O(n \log n / \log \log n)$ , query  $O(k + \log n)$ .
  - No address manipulations are required.
  - This factor can be removed in many variants of the problem—grounded rectangle, fixed aspect ratio, grounded trapezoid.

- Filtering search
  - Space  $O(n \log n / \log \log n)$ , query  $O(k + \log n)$ .
  - No address manipulations are required.
  - This factor can be removed in many variants of the problem—grounded rectangle, fixed aspect ratio, grounded trapezoid.
- In RAM, one can actually achieve same query time with O(n log<sup>ϵ</sup> n) space, for any fixed ϵ > 0.

- Filtering search
  - Space  $O(n \log n / \log \log n)$ , query  $O(k + \log n)$ .
  - No address manipulations are required.
  - This factor can be removed in many variants of the problem—grounded rectangle, fixed aspect ratio, grounded trapezoid.
- In RAM, one can actually achieve same query time with O(n log<sup>ϵ</sup> n) space, for any fixed ϵ > 0.
- Is this space/query tradeoff the best possible?

白 ト イ ヨ ト イ ヨ ト

In a nutshell: No address computation, indivisible storage.

回 とう ヨン うちとう

臣

In a nutshell: No address computation, indivisible storage.

- Memory is an unbounded collection of registers.
- Each register is a record with a fixed number of data and pointer fields.

In a nutshell: No address computation, indivisible storage.

- Memory is an unbounded collection of registers.
- Each register is a record with a fixed number of data and pointer fields.
- Thus the memory can be modeled as a directed graph with bounded outdegree.

In a nutshell: No address computation, indivisible storage.

- Memory is an unbounded collection of registers.
- Each register is a record with a fixed number of data and pointer fields.
- Thus the memory can be modeled as a directed graph with bounded outdegree.
- Increasing the storage by a multiplicative factor(duplicating nodes), we can assume outdegree is at most 2.
- Given {p<sub>1</sub>, · · · , p<sub>n</sub>}, a data structure for orthogonal range reporting is a digraph G = (V, E) with a source node σ.

回 とくほとくほど

- Each node v has a label, label(v) between 0 and n.
- If the label is *i*, then the node corresponds to point *p<sub>i</sub>*. If the label is 0, then the node is used internally by the data structure, not to report any point.
- Many nodes can be associated with the same point.

- Each node v has a label, label(v) between 0 and n.
- If the label is *i*, then the node corresponds to point *p<sub>i</sub>*. If the label is 0, then the node is used internally by the data structure, not to report any point.
- Many nodes can be associated with the same point.
- Given a query q, the algorithm starts traversing G at  $\sigma$ .

- Each node v has a label, label(v) between 0 and n.
- If the label is *i*, then the node corresponds to point *p<sub>i</sub>*. If the label is 0, then the node is used internally by the data structure, not to report any point.
- Many nodes can be associated with the same point.
- Given a query q, the algorithm starts traversing G at  $\sigma$ .
  - No node can be visited if a node pointing to it has not already been visited.
  - Completion cannot occur until each label *i* such that p<sub>i</sub> ∈ q has been encountered at least once.

- Algorithm can modify data and address fields of visited nodes.
- Can add new nodes from *freelist*.
- $N(v) = \{w | (v, w) \in E\}$ . Set  $W = \{\sigma\}$  initially.
  - Pick any  $v \in W$  and add N(v) to W.

- Algorithm can modify data and address fields of visited nodes.
- Can add new nodes from *freelist*.
- $N(v) = \{w | (v, w) \in E\}$ . Set  $W = \{\sigma\}$  initially.
  - Pick any  $v \in W$  and add N(v) to W.
  - Request a new node v from the freelist and add it to W. Initialize N(v) to Ø.

- Algorithm can modify data and address fields of visited nodes.
- Can add new nodes from *freelist*.
- $N(v) = \{w | (v, w) \in E\}$ . Set  $W = \{\sigma\}$  initially.
  - Pick any  $v \in W$  and add N(v) to W.
  - Request a new node v from the freelist and add it to W.
    Initialize N(v) to ∅.
  - Pick any v,w ∈ W and if |N(v)| < 2, add (v, w) to E (and v or w to V, if necessary).</li>

- Algorithm can modify data and address fields of visited nodes.
- Can add new nodes from *freelist*.
- $N(v) = \{w | (v, w) \in E\}$ . Set  $W = \{\sigma\}$  initially.
  - Pick any  $v \in W$  and add N(v) to W.
  - Request a new node v from the freelist and add it to W. Initialize N(v) to Ø.
  - Pick any  $v, w \in W$  and if |N(v)| < 2, add (v, w) to E (and v or w to V, if necessary).
  - Pick any v, w ∈ W and remove the edge (v, w) from E if it exists.

At the end W(q) must contain at least one node corresponding to each output point in the range q. Query time is measured as |W(q)|.

<回> < 三> < 三>

- Algorithm can modify data and address fields of visited nodes.
- Can add new nodes from *freelist*.
- $N(v) = \{w | (v, w) \in E\}$ . Set  $W = \{\sigma\}$  initially.
  - Pick any  $v \in W$  and add N(v) to W.
  - Request a new node v from the freelist and add it to W. Initialize N(v) to Ø.
  - Pick any  $v, w \in W$  and if |N(v)| < 2, add (v, w) to E (and v or w to V, if necessary).
  - Pick any v, w ∈ W and remove the edge (v, w) from E if it exists.

At the end W(q) must contain at least one node corresponding to each output point in the range q. Query time is measured as |W(q)|.

<回> < 三> < 三>

## Lower bound for ORR in PMM

Remarks: "Pick any",

臣

Remarks: "Pick any", why knowing *i* is not sufficient.

・日・ ・ ヨ・ ・ ヨ・

臣

Remarks: "Pick any", why knowing *i* is not sufficient. This is a big difference between RAM and a pointer machine.

• Let  $a, b \in \mathbb{R}^+$ . G = (V, E) is (a, b)-effective if for any query range q,  $|W(q)| \le a(|P \cap q| + \log^b n)$ .

Remarks: "Pick any", why knowing *i* is not sufficient. This is a big difference between RAM and a pointer machine.

- Let  $a, b \in \mathbb{R}^+$ . G = (V, E) is (a, b)-effective if for any query range q,  $|W(q)| \le a(|P \cap q| + \log^b n)$ .
- Main idea to get a lower bound:
  - Nodes corresponding to output points cannot be too spread out in the graph, otherwise it would be impossible to get them in linear time.
  - Let  $S = \{q_1, \cdots, q_s\}$  be a set of queries, and let  $\alpha > 0$ .
  - S is  $\alpha$ -favorable if, for each  $i, j(i \neq j)$ ,
  - $|P \cap q_i| \geq \log^{\alpha} n.$
  - $|P \cap q_i \cap q_j| \leq 1.$

同ト・モト・モト

#### Theorem

If the data structure is (a, b)-effective and the set of queries is b-favorable, then

$$|V| > |S| \frac{\log^b n}{2^{16a+4}},$$

for n large enough.

Thus the main task is to get a large set of queries, no two of which share too many points of P...

#### Theorem

If the data structure is (a, b)-effective and the set of queries is b-favorable, then

 $|V| > |S| \frac{\log^b n}{2^{16a+4}},$ 

for n large enough.

Thus the main task is to get a large set of queries, no two of which share too many points of P...Proof on board.